



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



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華凌光電股份有限公司



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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF101GTAAPLNGO#

<p>APPROVED BY:</p> <p>(FOR CUSTOMER USE ONLY)</p>	<p>PCB VERSION: _____</p> <p>DATA: _____</p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭
ISSUED DATE: 2017/09/11			



RECORDS OF REVISION

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2016/08/17		First issue
A	2016/10/05		Modify Summary Add Aspect Ratio
B	2016/12/05		Modify Contour Drawing.
C	2017/03/06		Modify Pin27.37=NC
D	2017/06/20		Modify Symbol name of VCOM/VIH/VIL and Description of pin Function.
E	2017/07/18		Modify contour drawing
F	2017/09/11		Modify Electrical Characteristics

Contents

- 1.Module Classification Information
- 2.Summary
- 3.General Specification
- 4.Absolute Maximum Ratings
- 5.Electrical Characteristics
- 6.Interface Timing Characteristics
- 7.Optical Characteristics
- 8.Interface
- 9.Reliability
- 10.Touch Panel Information
- 11.Contour Drawing
- 12.Other

1.Module Classification Information

W F 101 G T A A P L N G 0 #
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪ ⑫ ⑬

①	Brand : WINSTAR DISPLAY CORPORATION											
②	Display Type : F→TFT Type, J→Custom TFT											
③	Display Size : 10.1” TFT											
④	Model serials no.											
⑤	Backlight Type :	F→CCFL, White S→LED, High Light White				T→LED, White Z→Nichia LED, White						
⑥	LCD Polarize Type/ Temperature range/ Gray Scale Inversion Direction	A→Transmissive, N.T, IPS TFT C→Transmissive, N. T, 6:00 ; F→Transmissive, N.T,12:00 ; I→Transmissive, W. T, 6:00 K→Transflective, W.T,12:00 L→Transmissive, W.T,12:00 N→Transmissive, Super W.T, 6:00				Q→Transmissive, Super W.T, 12:00 R→Transmissive, Super W.T, O-TFT V→Transmissive, Super W.T, VA TFT X→Transmissive, W.T, VA TFT Y→Transmissive, W.T, IPS TFT Z→Transmissive, W.T, O-TFT						
⑦	A : TFT LCD B : TFT+FR+CONTROL BOARD C : TFT+FR+A/D BOARD D : TFT+FR+A/D BOARD+CONTROL BOARD E : TFT+FR+POWER BOARD					F : TFT+CONTROL BOARD G : TFT+FR H : TFT+D/V BOARD I : TFT+FR+D/V BOARD J : TFT+POWER BD						
⑧	Resolution:											
	A	128160	B	320234	C	320240	D	480234	E	480272	F	640480
	G	800480	H	1024600	I	320480	J	240320	K	800600	L	240400
	M	1024768	N	128128	P	1280800	Q	480800	R	640320	S	480128
	T	800320	U	8001280	V	176220	W	1280398	X	1024250	Y	1920720
	Z	800200	2	1024324	3	7201280						
⑨	D: Digital L : LVDS M:MIPI											
⑩	Interface : N:without control board A:8Bit B:16Bit H: HDMI I:I2C Interface R:RS232 S:SPI Interface U:USB											
⑪	TS : N : Without TS T : resistive touch panel C : capacitive touch panel (G-F-F) G : capacitive touch panel(G-G)											
⑫	Version											
⑬	Special Code	#:Fit in with ROHS directive regulations										

2.Summary

TFT 10.1” is a IPS transmissive type color active matrix TFT liquid crystal display . In-Plane Switching (IPS) was one of the first refinements to produce significant gains in the light-transmissive characteristics of TFT panels. It is a technology that addresses the two main issues of a standard twisted nematic (TN) TFT display: colour and viewing angle.

3. General Specifications

Item	Dimension	Unit
Screen Diagonal	10.1	inch
Number of Pixels	1280 x 3(RGB) x 800	dots
Module dimension	257.96 x 168.6 x 5.575	mm
Active area	216.96 (H) x 135.6(V)	mm
Pixel pitch	0.1695 × 0.1695	mm
Display Mode	Normally Black	
Pixel Arrangement	R.G.B. Vertical Stripe	
Backlight Type	LED, Normally White	
Aspect Ratio	16:9	
CTP FW Version	50	
Electrical Interface (Logic)	LVDS	
With /Without TP	With CTP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	0	—	+50	°C
Storage Temperature	TST	-20	—	+60	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. $\leq 60^{\circ}\text{C}$, 90% RH MAX. Temp. $> 60^{\circ}\text{C}$, Absolute humidity shall be less than 90% RH at 60°C

5. Electrical Characteristics

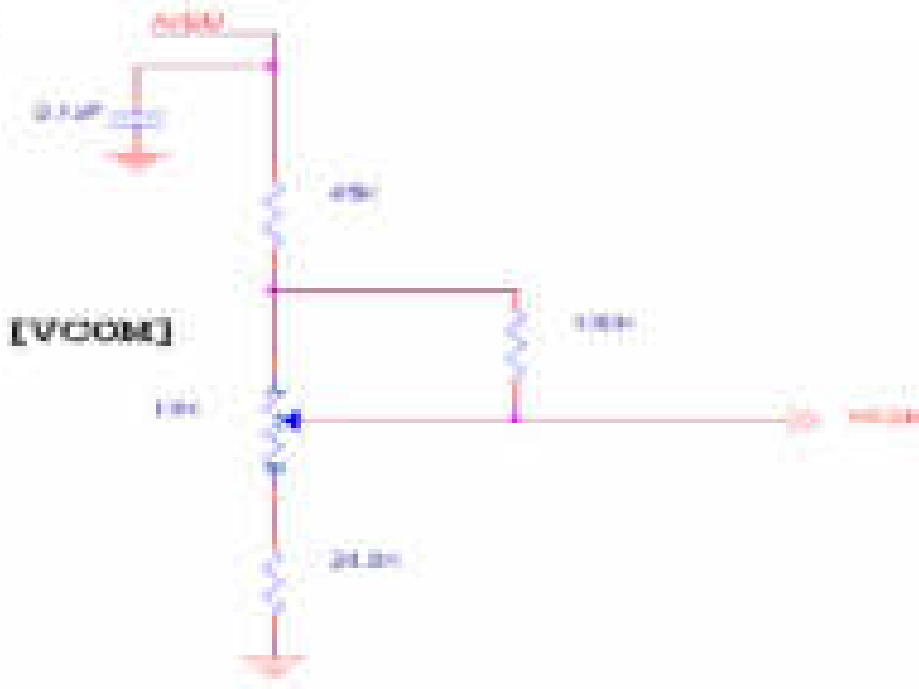
5.1. Typical Operation Conditions

(Note 1)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	MAX.		
Power voltage	VDD	2.3	2.5	2.7	V	
	AVDD	8.0	8.2	8.4	V	
	VGH	21.7	22	22.3	V	
	VGL	-7.3	-7	-6.7	V	
Input signal voltage	VCOM	2.7	3.0	3.3	V	Note 2
Supply Voltage For Touch Logic	VDDT	2.8	-	3.3	V	

Note 1: Be sure to apply VDD and VGL to the LCD first, and then apply VGH.

Note 2: Typical VCOM is only a reference value, it must be optimized according to each LCM. Be sure to use VR.

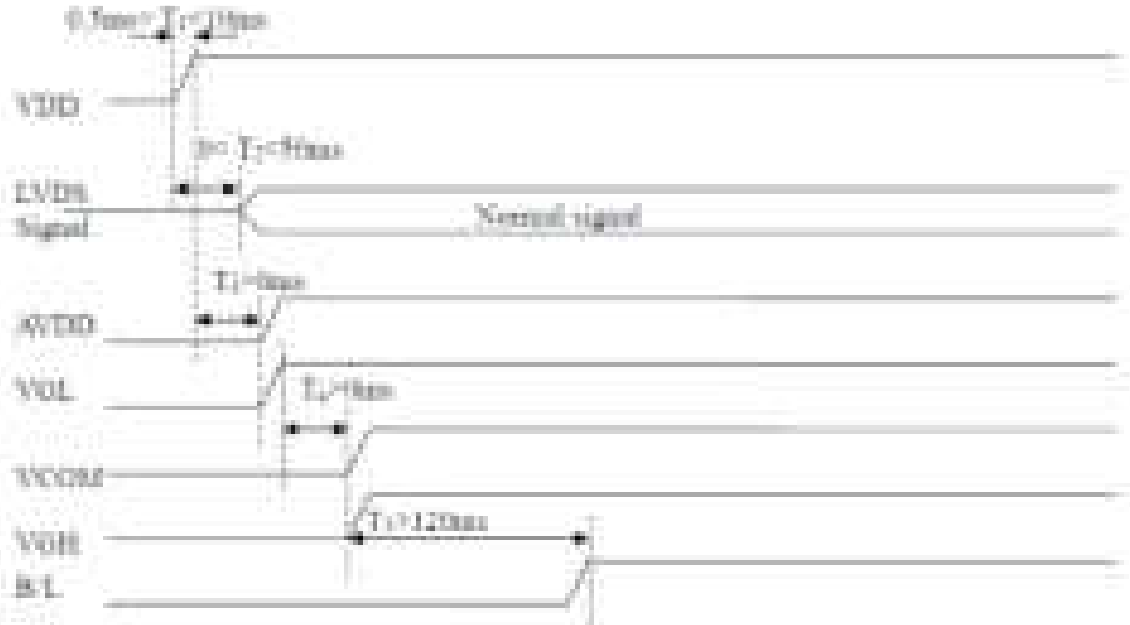


5.2. Current Consumption

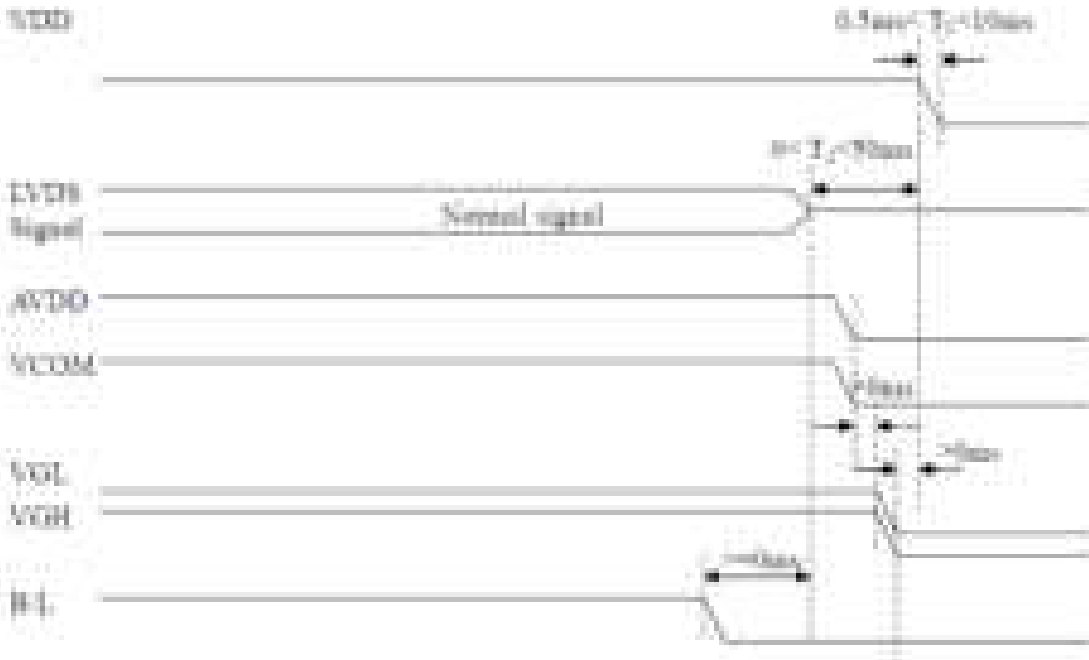
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	MAX.		
Current for Driver	IGH	-	705	750	uA	VGH =22V
	IGL	-	705	750	uA	VGL = -7V
	IVDD	-	95	120	mA	VDD =2.5V
	IAVDD	-	45	70	mA	AVDD=8.2V

5.3. Power Sequence

a. Power on:



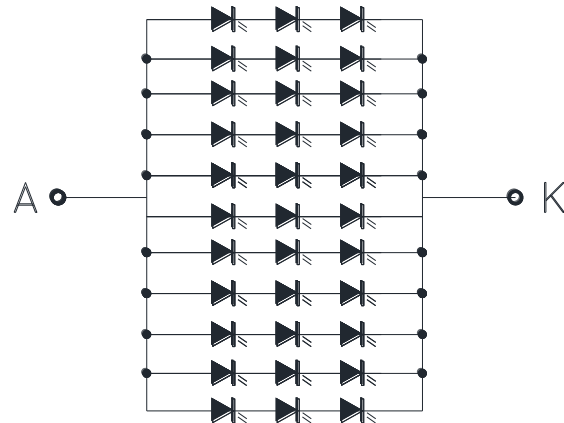
b. Power off:



5.4. Backlight Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Supply voltage of white LED backlight	VL	8.6	9.6	10.2	V	Note 1
Current for LED backlight	IL	—	220	—	mA	
LED life time	-	50000	-	-	Hr	Note 1

Note 1 : There are 1 Groups LED



Note 2 : $T_a = 25\text{ }^\circ\text{C}$

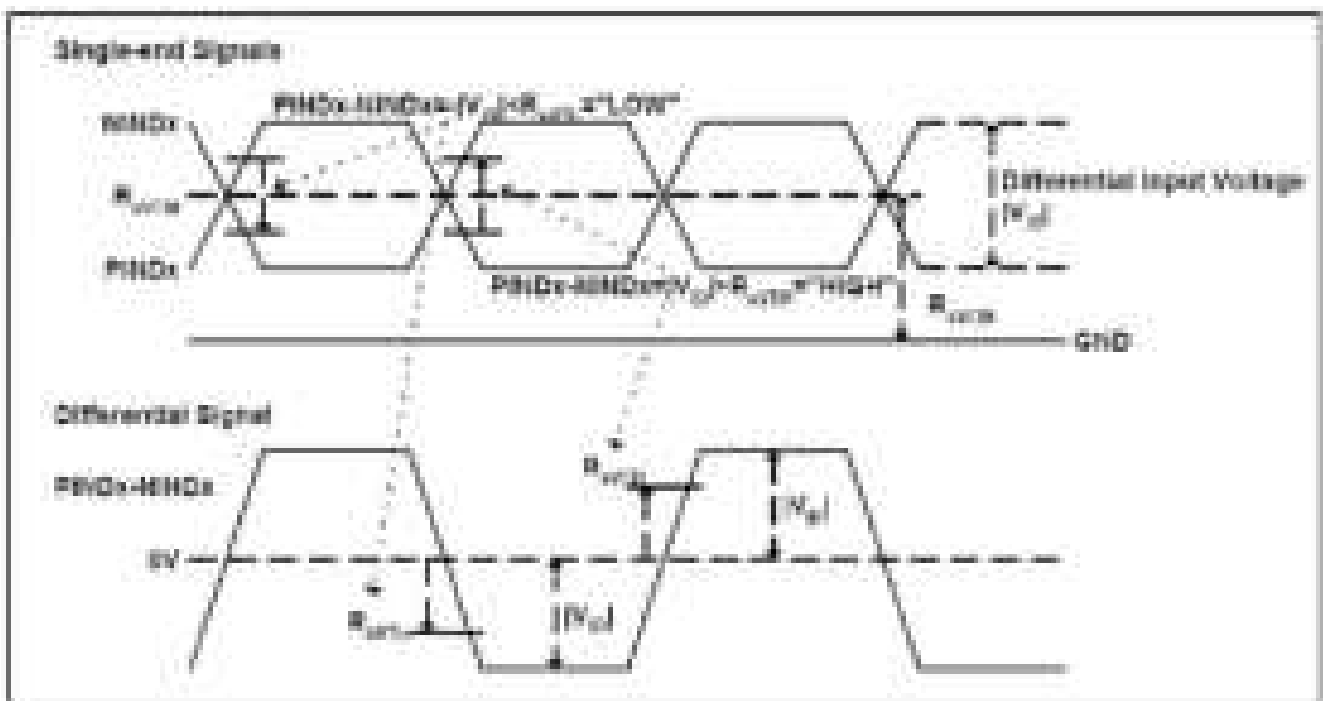
Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

6.LVDS Signal Timing Characteristics

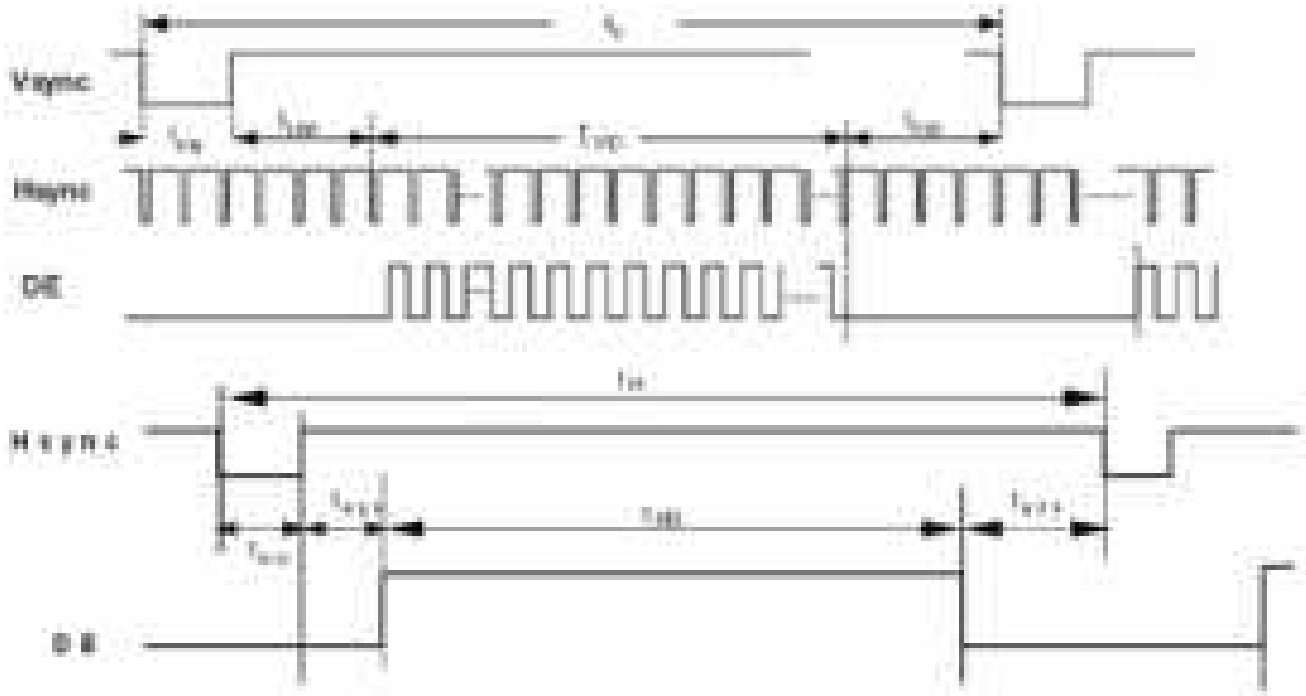
6.1. AC Electrical Characteristics

Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	MAX.		
LVDS Differential input high Threshold voltage	RxVTH	-	-	+100	mV	RXVCM=1.2 V
LVDS Differential input low Threshold voltage	RxVTL	-100	-	-	mV	
LVDS Differential input common mode voltage	RxVCM	0.7	-	1.6	V	
LVDS Differential voltage	VID	200	-	600	mV	

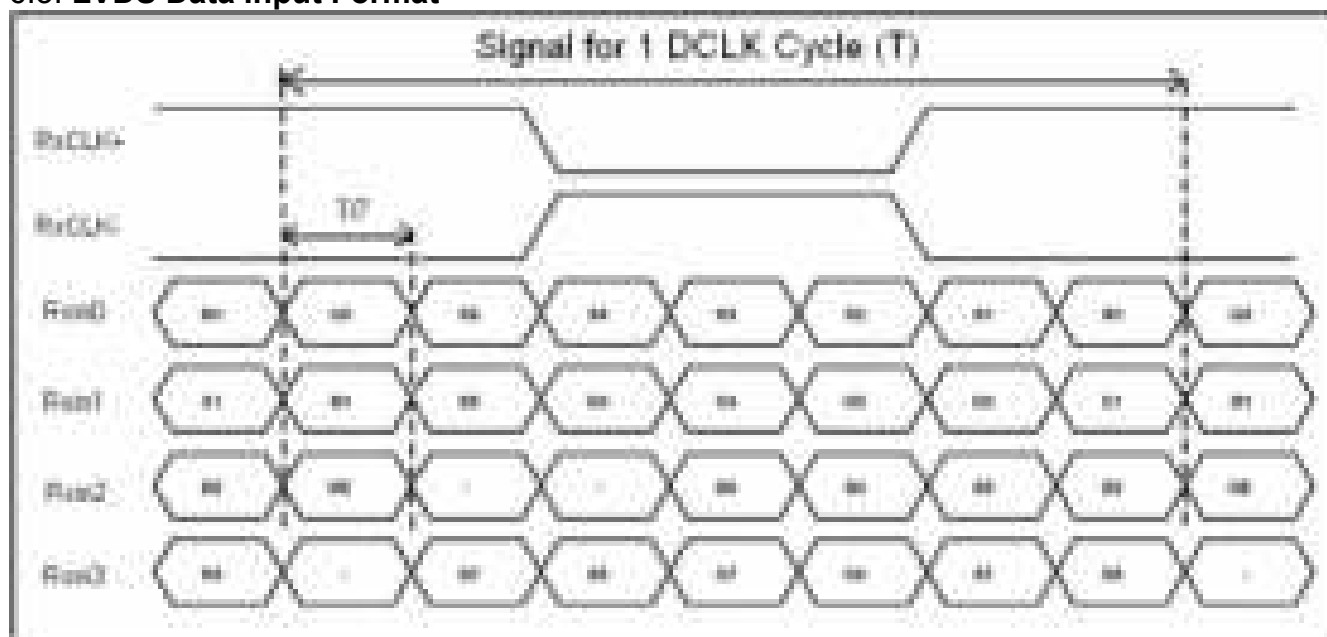


6.2. Timing Table

Parameter	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Clock Frequency	1/Tc	68.9	71.1	73.4	Mhz	Frame rate =60Hz
Horizontal display area	thd	1280			Tc	
HS period time	th	1410	1440	1470	Tc	
HS Width +Back Porch +Front Porch	tHW+ tHBP +tHFP	60	160	190	Tc	
Vertical display area	tvd	800			tH	
VS period time	tv	815	823	833	tH	
VS Width +Back Porch +Front Porch	tvW+ tvBP +tvFP	15	23	33	tH	



6.3. LVDS Data Input Format



7. Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark	
Response time	Tr	$\theta = 0^\circ$ 、 $\Phi = 0^\circ$	-	10	20	.ms	Note 3	
	Tf		-	15	30			
Contrast ratio	CR	At optimized viewing angle	600	800	-	-	Note 4	
Color Chromaticity	White	$\theta = 0^\circ$ 、 $\Phi = 0^\circ$	Wx	0.26	0.31	0.36	-	Note 2,5
			Wy	0.28	0.33	0.38	-	
Viewing angle (Gray Scale Inversion Direction)	Hor.	$CR \geq 10$	Θ_R	75	85	-	Deg.	Note 1
			Θ_L	75	85	-		
	Ver.		Φ_T	75	85	-		
			Φ_B	75	85	-		
Brightness	-	-	300	-	-	cd/m ²	Center of display	

Ta=25±2°C

Note 1: Definition of viewing angle range

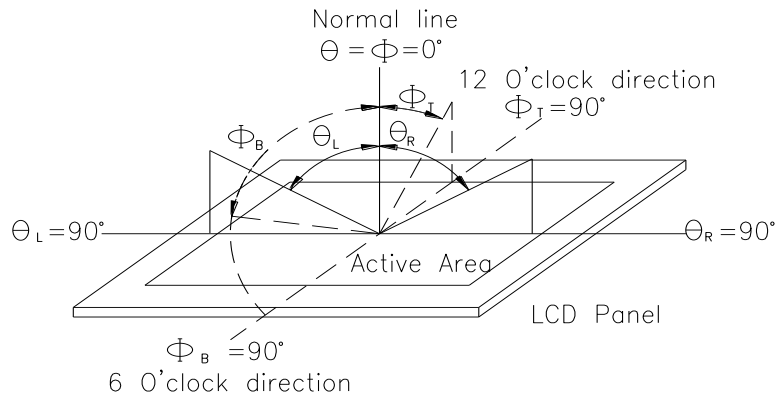


Fig. 7.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

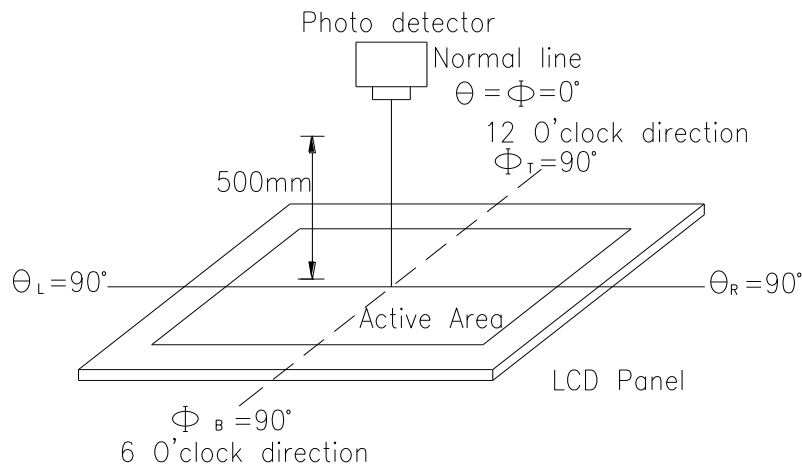
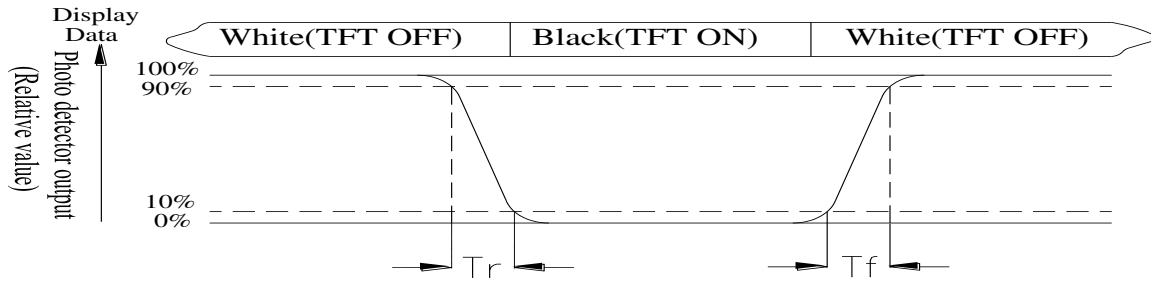


Fig. 7.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White” state and

“Black” state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White $V_i = V_{i50} \pm 1.5V$

Black $V_i = V_{i50} \pm 2.0V$

“±” means that the analog input signal swings in phase with VCOM signal.

“±” means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

8.Interface

8.1. Interface Connector

A 40pin connector is used for the module electronics interface. The recommended model is F62240-H1210B manufactured by Vigorconn.

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	P	Common Voltage	
2	VDD	P	Power Supply	
3	VDD	P	Power Supply	
4	NC	-	No connection	
5	NC	-	No connection	
6	NC	-	No connection	
7	GND	P	Ground	
8	Rxin0-	I	-LVDS Differential Data Input	R0-R5,G0
9	Rxin0+	I	+LVDS Differential Data Input	
10	GND	P	Ground	
11	Rxin1-	I	-LVDS Differential Data Input	G1G5,B0,B1
12	Rxin1+	I	+LVDS Differential Data Input	
13	GND	P	Ground	
14	Rxin2-	I	-LVDS Differential Data Input	B2-B5,HS,VS, DE
15	Rxin2+	I	+LVDS Differential Data Input	
16	GND	P	Ground	
17	RxCLK-	I	-LVDS Differential Clock Input	LVDS CLK
18	RxCLK+	I	+LVDS Differential Clock Input	
19	GND	P	Ground	
20	Rxin3-	I	-LVDS Differential Data Input	R6,R7,G6,G7, B6,B7
21	Rxin3+	I	+LVDS Differential Data Input	
22	GND	P	Ground	
23	NC	-	No connection	
24	NC	-	No connection	
25	GND	P	Ground	
26	NC	-	No connection	
27	NC	-	No connection	
28	NC	-	No connection	
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	

31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	NC	-	No connection	
34	NC	-	No connection	
35	VGL	P	Gate OFF Voltage	
36	NC	-	No connection	
37	NC	-	No connection	
38	VGH	P	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I: input, O: output, P: Power

8.2. CTP PIN Definition

Pin	Symbol	Function	Remark
1	SDA	I2C data input and output	
2	SCL	I2C clock input	
3	/RST	External Reset, Low is active	
4	/INT	External interrupt to the host	
5	VDDT	Power Supply : +3.3V	
6	VSS	Ground for analog circuit	

9. Reliability

Content of Reliability Test (Wide temperature, 0°C~50°C)

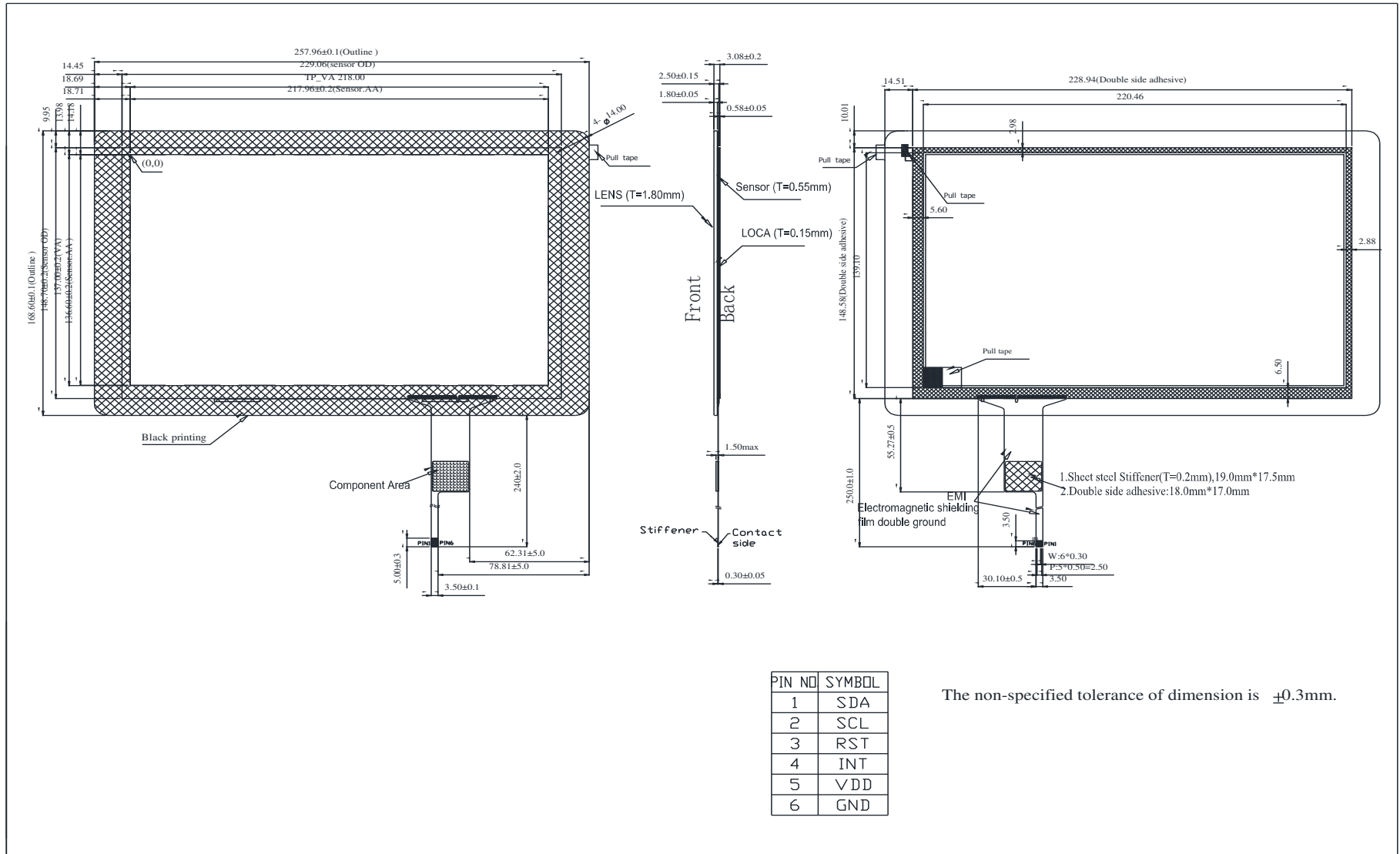
Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	60°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-20°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	50°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	0°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 °C, 90%RH max	60°C, 90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation <div style="text-align: center;"> <p style="margin-left: 100px;">0°C 25°C 50°C</p> <p style="margin-left: 100px;">30min 5min 30min</p> <p style="margin-left: 100px;">1 cycle</p> </div>	0°C/50°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

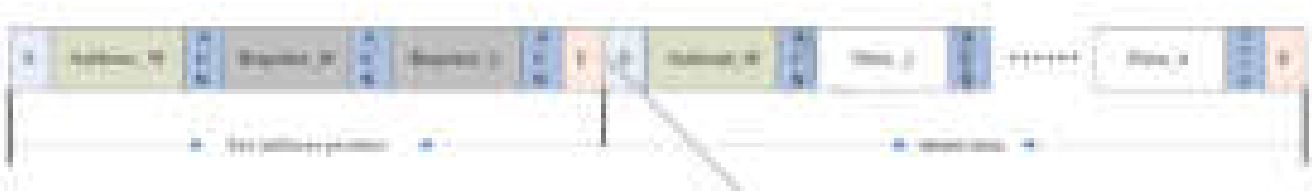
10.Touch Panel Information



10.1. Timing for Read Operation

First, set address pointer based on the aforesaid Write Operation timing sequence. Then, resend

Start condition to perform Read addressing and read data in the registers.



Address_R: Slave address with Read control bit.

NACK: Host issues NACK after reading the last byte.

After setting Read addresses, the host can read one or more than one byte at a time. GT9110 will automatically increase the address pointer and send subsequent data in sequence.

The Stop condition (the first E signal as shown in the above diagram) after setting the address pointer is optional. However, the repeated Start condition has to be sent.

10.2. Coordinate registers

Addr	Access	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
0x140	R								Product ID first byte (A0CB)
0x141	R								Product ID second byte (A0CB)
0x142	R								Product ID third byte (A0CB)
0x143	R								Product ID fourth byte (A0CB)
0x144	R								Firmware version (HEX low byte)
0x145	R								Firmware version (HEX high byte)
0x146	R								x coordinate resolution (low byte)
0x147	R								x coordinate resolution (high byte)
0x148	R								y coordinate resolution (low byte)
0x149	R								y coordinate resolution (high byte)

0x014A	R	Vendor_ID(0 of the current module)				
0x014B	R	Reserved				
0x014C	R	Reserved				
0x014D	R	Reserved				
0x014E	R/W	buffer status	large detect	Reserved	Have they	number of touch points
0x014F	R	track size 12 indicates left independently detection signal				
0x0150	R	Flag04	Reserved			
0x0151	R	Flag04	Reserved			
0x0152	R	Reserved				
0x0153	R	Reserved				
0x0154	R	Reserved				
0x0155	R	Reserved				
0x0156	R	Reserved				
0x0157	R	pin_004	Reserved			track id
0x0158	R	point 1 x coordinate (low byte)				
0x0159	R	point 1 x coordinate (high byte)				
0x015A	R	point 1 y coordinate (low byte)				
0x015B	R	point 1 y coordinate (high byte)				
0x015C	R	Point 1 size (low byte)				
0x015D	R	point 1 size (high byte)				
0x015E	R	Reserved				
0x015F	R	track id				
0x0160	R	point 2 x coordinate (low byte)				
0x0161	R	point 2 x coordinate (high byte)				
0x0162	R	point 2 y coordinate (low byte)				
0x0163	R	point 2 y coordinate (high byte)				
0x0164	R	point 2 size (low byte)				
0x0165	R	point 2 size (high byte)				
0x0166	R	Reserved				
0x0167	R	track id				
0x0168	R	point 3 x coordinate (low byte)				
0x0169	R	point 3 x coordinate (high byte)				
0x016A	R	point 3 y coordinate (low byte)				
0x016B	R	point 3 y coordinate (high byte)				
0x016C	R	point 3 size (low byte)				
0x016D	R	point 3 size (high byte)				
0x016E	R	Reserved				
0x016F	R	track id				
0x0170	R	point 4 x coordinate (low byte)				
0x0171	R	point 4 x coordinate (high byte)				
0x0172	R	point 4 y coordinate (low byte)				

0x0173	R	point 4 x coordinate (high byte)
0x0174	R	point 4 size (low byte)
0x0175	R	point 4 size (high byte)
0x0176	R	Reserved
0x0177	R	track id
0x0178	R	point 5 x coordinate (low byte)
0x0179	R	point 5 x coordinate (high byte)
0x017A	R	point 5 y coordinate (low byte)
0x017B	R	point 5 y coordinate (high byte)
0x017C	R	point 5 size (low byte)
0x017D	R	point 5 size (high byte)
0x017E	R	Reserved
0x017F	R	track id
0x0180	R	point 6 x coordinate (low byte)
0x0181	R	point 6 x coordinate (high byte)
0x0182	R	point 6 y coordinate (low byte)
0x0183	R	point 6 y coordinate (high byte)
0x0184	R	point 6 size (low byte)
0x0185	R	point 6 size (high byte)
0x0186	R	Reserved
0x0187	R	track id
0x0188	R	point 7 x coordinate (low byte)
0x0189	R	point 7 x coordinate (high byte)
0x018A	R	point 7 y coordinate (low byte)
0x018B	R	point 7 y coordinate (high byte)
0x018C	R	point 7 size (low byte)
0x018D	R	point 7 size (high byte)
0x018E	R	Reserved
0x018F	R	track id
0x0190	R	point 8 x coordinate (low byte)
0x0191	R	point 8 x coordinate (high byte)
0x0192	R	point 8 y coordinate (low byte)
0x0193	R	point 8 y coordinate (high byte)
0x0194	R	point 8 size (low byte)
0x0195	R	point 8 size (high byte)
0x0196	R	Reserved
0x0197	R	track id
0x0198	R	point 9 x coordinate (low byte)
0x0199	R	point 9 x coordinate (high byte)
0x019A	R	point 9 y coordinate (low byte)
0x019B	R	point 9 y coordinate (high byte)

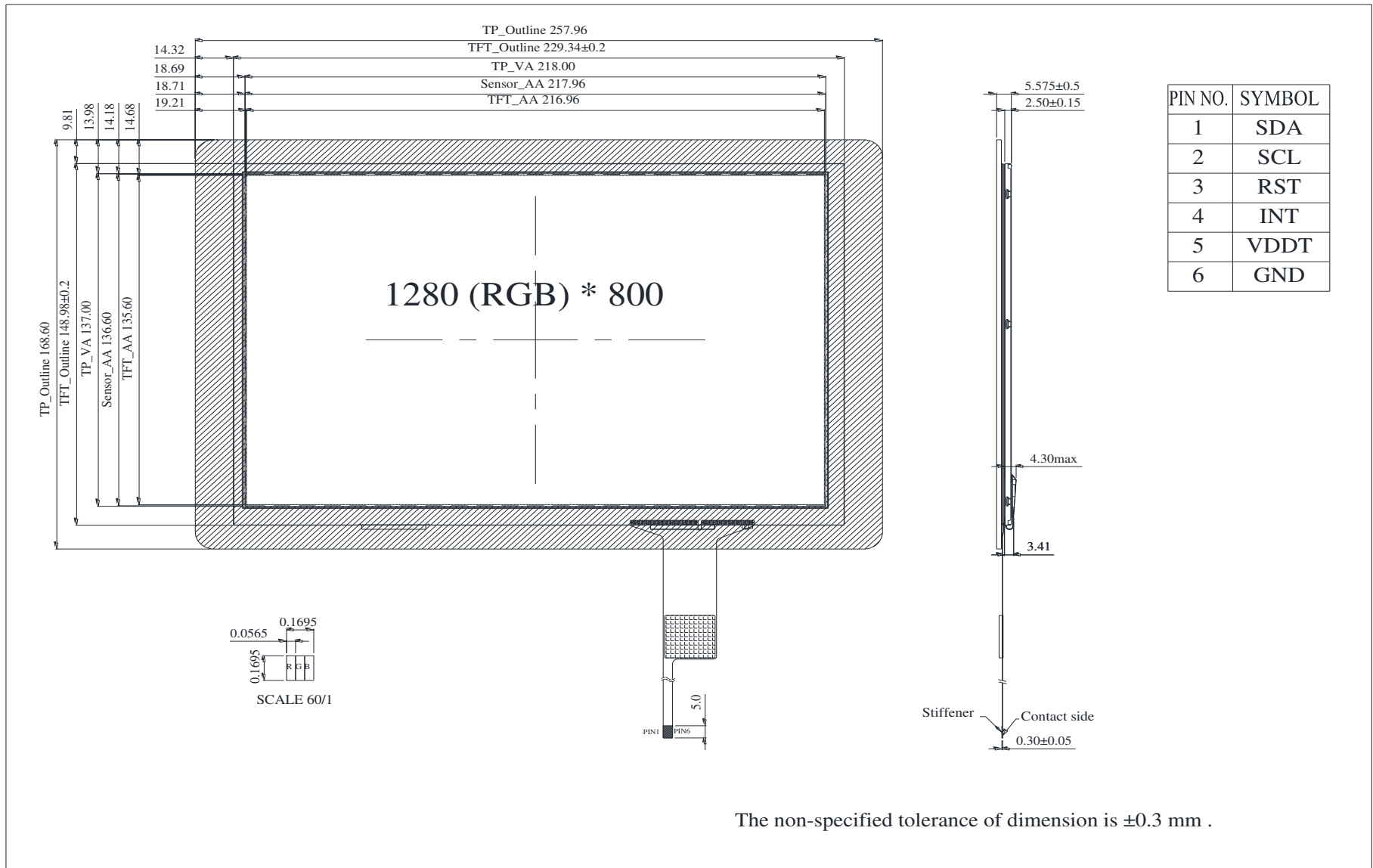
0x019C	R	point 8 size (low byte)
0x019D	R	point 8 size (high byte)
0x019E	R	Reserved
0x019F	R	mask 8
0x01A0	R	point 10 x coordinate (low byte)
0x01A1	R	point 10 x coordinate (high byte)
0x01A2	R	point 10 y coordinate (low byte)
0x01A3	R	point 10 y coordinate (high byte)
0x01A4	R	point 10 size (low byte)
0x01A5	R	point 10 size (high byte)
0x01A6	R	Reserved
0x01A7	R	keyvalue

10.3. I2C protocol

3.3V ,400BPS ,pull high 2K ohm

Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_L	0.9	-	μs
SCL high period	t_H	0.8	-	μs
SCL setup time for START condition	t_{SU1}	0.4	-	μs
SCL setup time for STOP condition	t_{SU2}	0.4	-	μs
SCL hold time for START condition	t_{HD1}	0.3	-	μs
SDA setup time	t_{SD}	0.4	-	μs
SDA hold time	t_{SD}	0.4	-	μs

11. Contour Drawing





1、Panel Specification :

- 1. Panel Type : Pass NG , _____
- 2. View Direction : Pass NG , _____
- 3. Numbers of Dots : Pass NG , _____
- 4. View Area : Pass NG , _____
- 5. Active Area : Pass NG , _____
- 6. Operating Temperature : Pass NG , _____
- 7. Storage Temperature : Pass NG , _____
- 8. Others : _____

2、Mechanical Specification :

- 1. PCB Size : Pass NG , _____
- 2. Frame Size : Pass NG , _____
- 3. Material of Frame : Pass NG , _____
- 4. Connector Position : Pass NG , _____
- 5. Fix Hole Position : Pass NG , _____
- 6. Backlight Position : Pass NG , _____
- 7. Thickness of PCB : Pass NG , _____
- 8. Height of Frame to PCB : Pass NG , _____
- 9. Height of Module : Pass NG , _____
- 10. Others : Pass NG , _____

3、Relative Hole Size :

- 1. Pitch of Connector : Pass NG , _____
- 2. Hole size of Connector : Pass NG , _____
- 3. Mounting Hole size : Pass NG , _____
- 4. Mounting Hole Type : Pass NG , _____
- 5. Others : Pass NG , _____

4、Backlight Specification :

- 1. B/L Type : Pass NG , _____
- 2. B/L Color : Pass NG , _____
- 3. B/L Driving Voltage (Reference for LED Type) : Pass NG , _____
- 4. B/L Driving Current : Pass NG , _____
- 5. Brightness of B/L : Pass NG , _____
- 6. B/L Solder Method : Pass NG , _____
- 7. Others : Pass NG , _____

>> **Go to page 2** <<



Winstar Module Number : _____

Page: 2

5、Electronic Characteristics of Module :

- | | | |
|------------------------------|-------------------------------|-------------------------------------|
| 1. Input Voltage : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Supply Current : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Driving Voltage for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Contrast for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. B/L Driving Method : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Negative Voltage Output : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Interface Function : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. LCD Uniformity : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. ESD test : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

6、Summary :

Sales signature : _____

Customer Signature : _____

Date : / / _____