UNISONIC TECHNOLOGIES CO., LTD

12N65K-MT Power MOSFET

12A, 650V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **12N65K-MT** are N-Channel enhancement mode power field effect transistors (MOSFET) which are produced by using UTC's proprietary, planar stripe and DMOS technology.

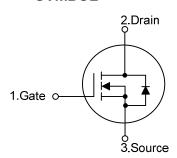
These devices are suited for high efficiency switch mode power supply. To minimize on-state resistance, provide superior switching performance and withstand high energy pulse in the avalanche and commutation mode, the advanced technology has been especially tailored.

■ FEATURES

- * $R_{DS(ON)}$ < 0.75 Ω @ V_{GS} = 10 V, I_{D} = 6 A
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

TO-220F1 TO-220F1 TO-220F2 TO-220F3 TO-263

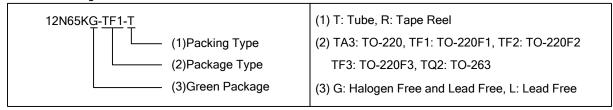
■ SYMBOL



■ ORDERING INFORMATION

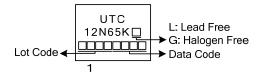
Ordering Number		Dealtage	Pin Assignment			Daaldaa	
Lead Free	Halogen Free	Package	1	2	3	Packing	
12N65KL-TA3-T	12N65KG-TA3-T	TO-220	G	D	S	Tube	
12N65KL-TF1-T	12N65KG-TF1-T	TO-220F1	G	D	S	Tube	
12N65KL-TF2-T	12N65KG-TF2-T	TO-220F2	G	D	S	Tube	
12N65KL-TF3-T	12N65KG-TF3-T	TO-220F3	G	D	S	Tube	
12N65KL-TQ2-T	12N65KG-TQ2-T	TO-263	G	D	S	Tube	
12N65KL-TQ2-R	12N65KG-TQ2-R	TO-263	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ MARKING



12N65K-MT

■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	650	V
Gate-Source Voltage		V_{GSS}	±30	V
Drain Current	Continuous	I_D	12	Α
	Pulsed (Note 2)	I _{DM}	48	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	400	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.7	V/ns
Power Dissipation	TO-220/TO-263		225	W
	TO-220F1/TO-220F2 TO-220F3	P_D	51	W
Junction Temperature		T_J	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 5.55mH, I_{AS} = 12A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 4. $I_{SD} \le 12A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$ Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		θ_{JA}	62.5	°C/W
Junction to Case	TO-220/TO-263	θЈС	0.56	°C/W
	TO-220F1/TO-220F2 TO-220F3		2.43	°C/W

Power MOSFET

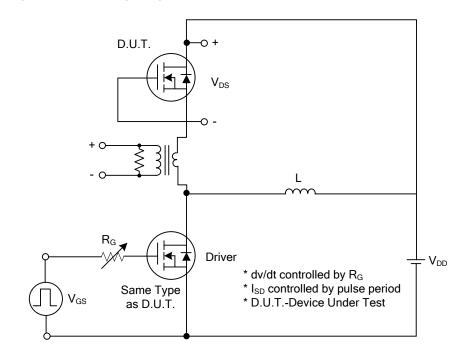
■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	650			V		
Drain-Source Leakage Current	I _{DSS}	$V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ		
Gate-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA		
Breakdown Voltage Temperature Coefficient	$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA,Referenced to 25°C		0.7		V/°C		
ON CHARACTERISTICS								
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V		
Static Drain-Source On-State Resistance	R _{DS(ON)}	$V_{GS} = 10V, I_D = 6.0A$		0.60	0.75	Ω		
DYNAMIC CHARACTERISTICS								
Input Capacitance	C _{ISS}	-V _{DS} = 25 V, V _{GS} = 0 V, -f = 1MHz		1600	1900	pF		
Output Capacitance	Coss			175	210	pF		
Reverse Transfer Capacitance	C _{RSS}			10	22	pF		
SWITCHING CHARACTERISTICS								
Total Gate Charge	Q_{G}	\\ - F0\\ - 4.2A		39	54	nC		
Gate-Source Charge	Q_GS	V _{DS} = 50V,I _D = 1.3A, V _{GS} = 10 V (Note 1, 2)		10		nC		
Gate-Drain Charge	Q_GD			9		nC		
Turn-On Delay Time	t _{D(ON)}	$V_{DD} = 30V, I_D = 0.5A,$ $R_G = 25\Omega \text{ (Note 1, 2)}$		100	110	ns		
Turn-On Rise Time	t _R			125	138	ns		
Turn-Off Delay Time	t _{D(OFF)}			180	230	ns		
Turn-Off Fall Time	t _F			104	140	ns		
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS								
Maximum Continuous Drain-Source Diode					40	۸		
Forward Current	I _S				12	Α		
Maximum Pulsed Drain-Source Diode	ı				48	^		
Forward Current	I _{SM}				40	Α		
Drain-Source Diode Forward Voltage	V_{SD}	V _{GS} = 0 V, I _S = 12A			1.4	V		
Reverse Recovery Time	t _{rr}	I _S =12A, V _{GS} =0V		590		ns		
Reverse Recovery Charge	Qrr	di/dt=100A/µs (Note 1)		6.2		μC		

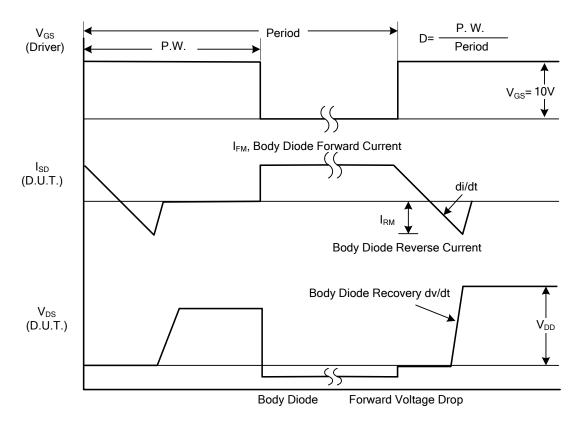
Notes: 1. Pulse Test : Pulse width ≤300µs, Duty cycle ≤ 2%

^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS



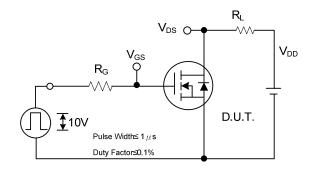
Peak Diode Recovery dv/dt Test Circuit

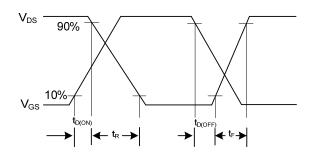


Peak Diode Recovery dv/dt Waveforms

12N65K-MT Power MOSFET

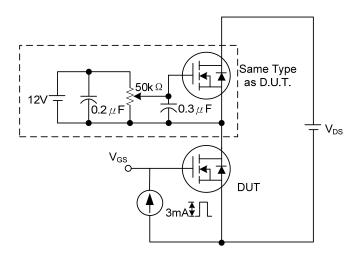
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

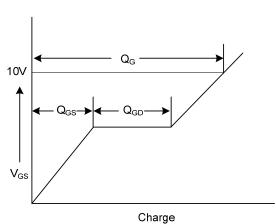




Switching Test Circuit

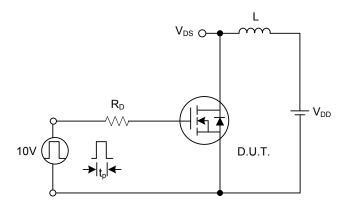
Switching Waveforms

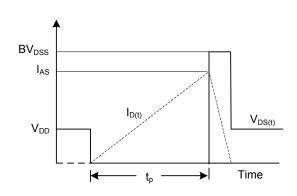




Gate Charge Test Circuit

Gate Charge Waveform

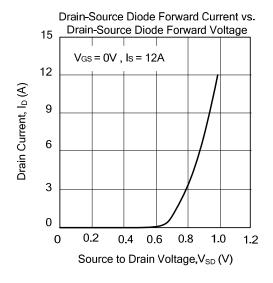


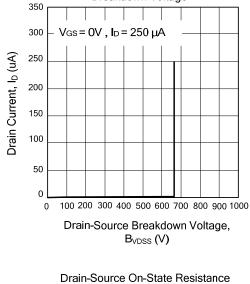


Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

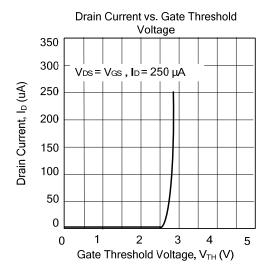
■ TYPICAL CHARACTERISTICS

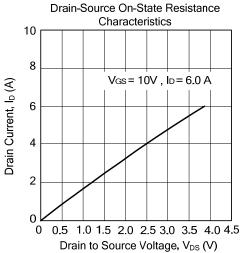


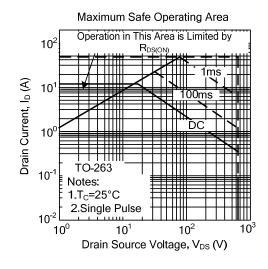


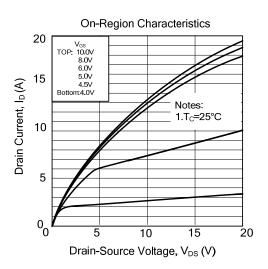
Drain Current vs. Drain-Source

Breakdown Voltage









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