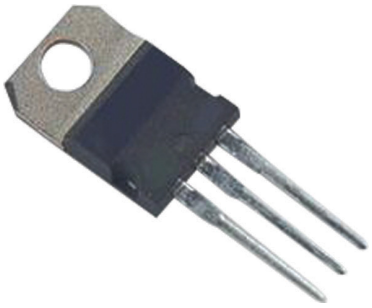
High Voltage Power Transistor



Description:

High Voltage NPN Silicon Power Transistors are designed for line operated audio output amplifier, and switching power supply drivers applications.

Features:

- Collector-Emitter sustaining voltage- 250V - 400V (Min.)
- 1A Rated collector current
- f_T = 10MHz (Min.) at I_C = 200mA

Maximum Ratings

Characteristic	Symbol	TIP47	TIP50	Unit
Collector-Emitter Voltage	V_{CEO}	250	400	V
Collector-Base Voltage	V_{CBO}	350	500	
Emitter-Base Voltage	V_{EBO}	5		
Collector Current-Continuous -Peak	I_C	1 2		A
Base Current	I_B	0.6		
Total Power Dissipation at T_C = 25°C Derate above 25°C	P_D	40 0.32		W W/°C
Operation and Storage Junction Temperature Range	T_J, T_{STG}	-65 to +150		°C

Thermal Characteristics

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	$R_{\theta jc}$	3.125	°C/W

Electrical Characteristics (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min.	Max.	Unit
OFF Characteristics				
Collector-Emitter Sustaining Voltage (1) I_C = 30mA, I_B = 0 TIP47 TIP50	$V_{CEO(sus)}$	250 400	-	V
Collector Cut off Current V_{CE} = 150V, I_B = 0 TIP47 V_{CE} = 300V, I_B = 0 TIP50	I_{CEO}	-	1	mA
Collector Cut off Current V_{CE} = 350V, V_{EB} = 0 TIP47 V_{CE} = 500V, V_{EB} = 0 TIP50	I_{CES}	-	1	
Emitter Cut off Current V_{EB} = 5V, I_C = 0	I_{EBO}	-	1	
ON Characteristics (1)				
DC Current Gain I_C = 0.3A, V_{CE} = 10V I_C = 1.0A, V_{CE} = 10V	h_{FE}	30 10	150	-
Collector-Emitter Saturation Voltage I_C = 1A, I_B = 200mA	$V_{CE(sat)}$	-	1	V
Base-Emitter On Voltage I_C = 1A, V_{CE} = 10V	$V_{BE(on)}$	-	1.5	
Dynamic Characteristics				
Current Gain-Bandwidth Product (2) I_C = 200mA, V_{CE} = 10V, f_{TEST} = 2MHz	f_T	10	-	MHz
Small Signal Current Gain I_C = 200mA, V_{CE} = 10V, f = 1kHz	h_{FE}	25	-	-

(1) Pulse Test: Pulse width ≤300μs, Duty Cycle ≤2%

(2) $f_T = h_{FE} \bullet f_{TEST}$

Figure - 1 Power Derating

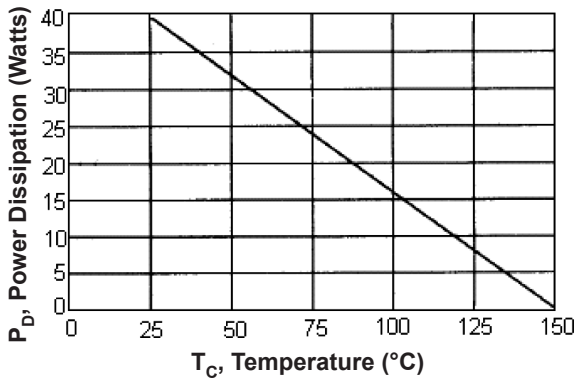


Figure - 2 DC Current Gain

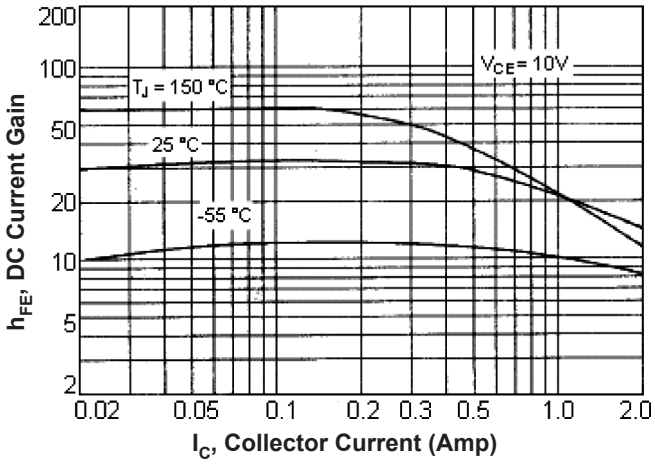


Figure - 3 Turn-On Time

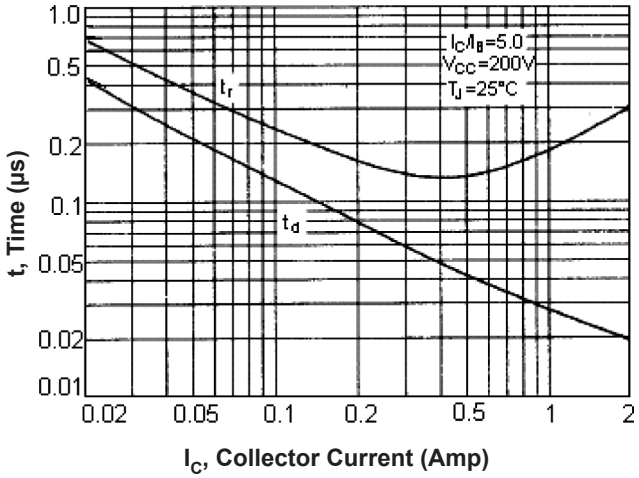


Figure - 4 “ON” Voltages

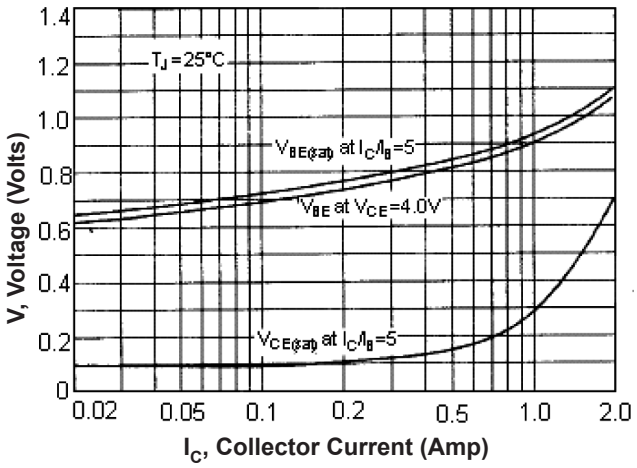


Figure - 5 Turn-Off Time

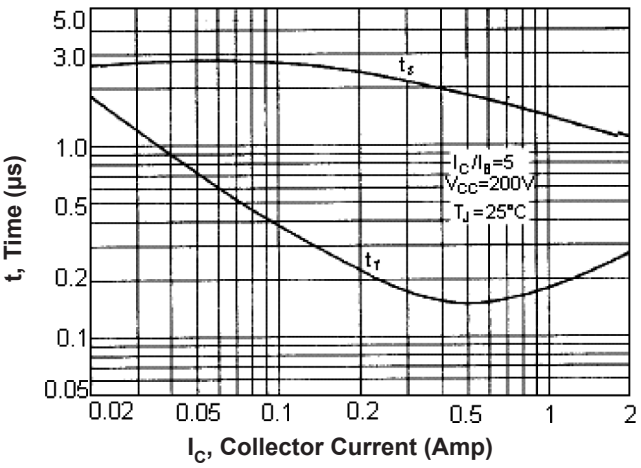
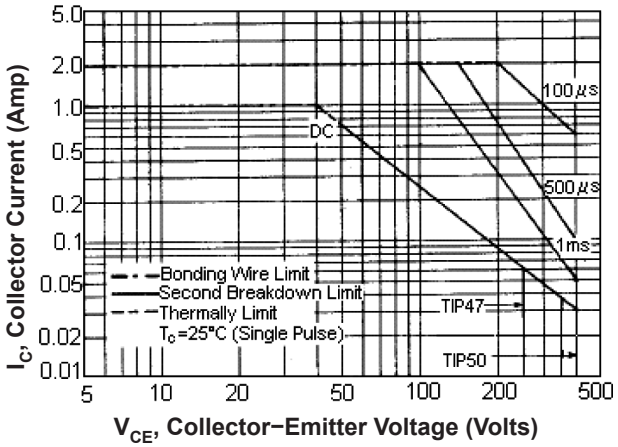


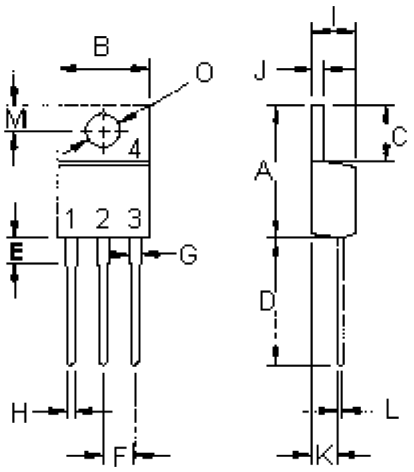
Figure - 6 Active Region Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure - 6 curve is based on $T_{J(PK)}$ = 150°C; T_C is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)}$ ≤150°C. At high case temperatures, thermal limitation will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

High Voltage Power Transistor



Pin Configuration:

1. Base
2. Collector
3. Emitter
4. Collector(Case)

Dimensions	Min.	Max.
A	14.68	15.31
B	9.78	10.42
C	5.01	6.52
D	13.06	14.62
E	3.57	4.07
F	2.42	3.66
G	1.12	1.36
H	0.72	0.96
I	4.22	4.98
J	1.14	1.38
K	2.2	2.97
L	0.33	0.55
M	2.48	2.98
O	3.7	3.9

Dimensions : Millimetres

Part Number Table

Description	Part Number
Transistor, NPN, TO-220	TIP47
	TIP50