

LA5016 is a high-performance logic analyzer with 16 channels and 500M sampling rate. It is composed of two parts: software on personal computer and hardware equipment. It has the advantages of high sampling rate, large sampling depth, easy to use, etc. LA5016 can sample 16 digital signal at same time. Then the sampled data can be displayed, analyzed, exported and saved on the computer. Software can also decode the data if it conform to the standard protocol of the software supported. Then the decoded data can be displayed, exported and saved.

Features:

Portable and lightweight

500M sampling rate @ full channels

Large sampling depth and support compression

The built-in PWM generator

Compatible USB2.0/3.0 interface

Powerful software and easy to use

Support online upgrade automatically

Specification:

Input channels number: 16

Max sampling rate: 500M

Measurement bandwidth: 80M

Min pulse width can be captured: 6.25ns

Hardware memory size: 2Gbits

Hardware sampling depth: 100M/channel

Max compressed depth: 10G/channel

Input voltage range: -50V ~ +50V

Input impedance: 220K Ω , 12pF

Adjustable threshold voltage: -4V ~ +4V, step: 0.01V

PWM channels number: 2

PWM frequency range: 0.1 ~ 20MHz

PWM frequency adjust step: 10ns

PWM pulse width adjust step: 5ns

PWM output voltage: +3.3V

PWM output impedance: 50 Ω

Standby current: 200mA

Max operating current: 400mA

Support OS: Windows XP, Vista, Windows 7/8/10(32bit/64bit).MAC OS,LINUX

Supported standard protocols: UART/RS-232/485, I2C, SPI, CAN, DMX512, HDMI CEC, I2S/PCM, JTAG, LIN, Manchester, Modbus, 1-Wire, UNI/O, SDIO, SMBus, USB1.1, PS/2, NEC InfraRed, Parallel, etc...

High quality accessories



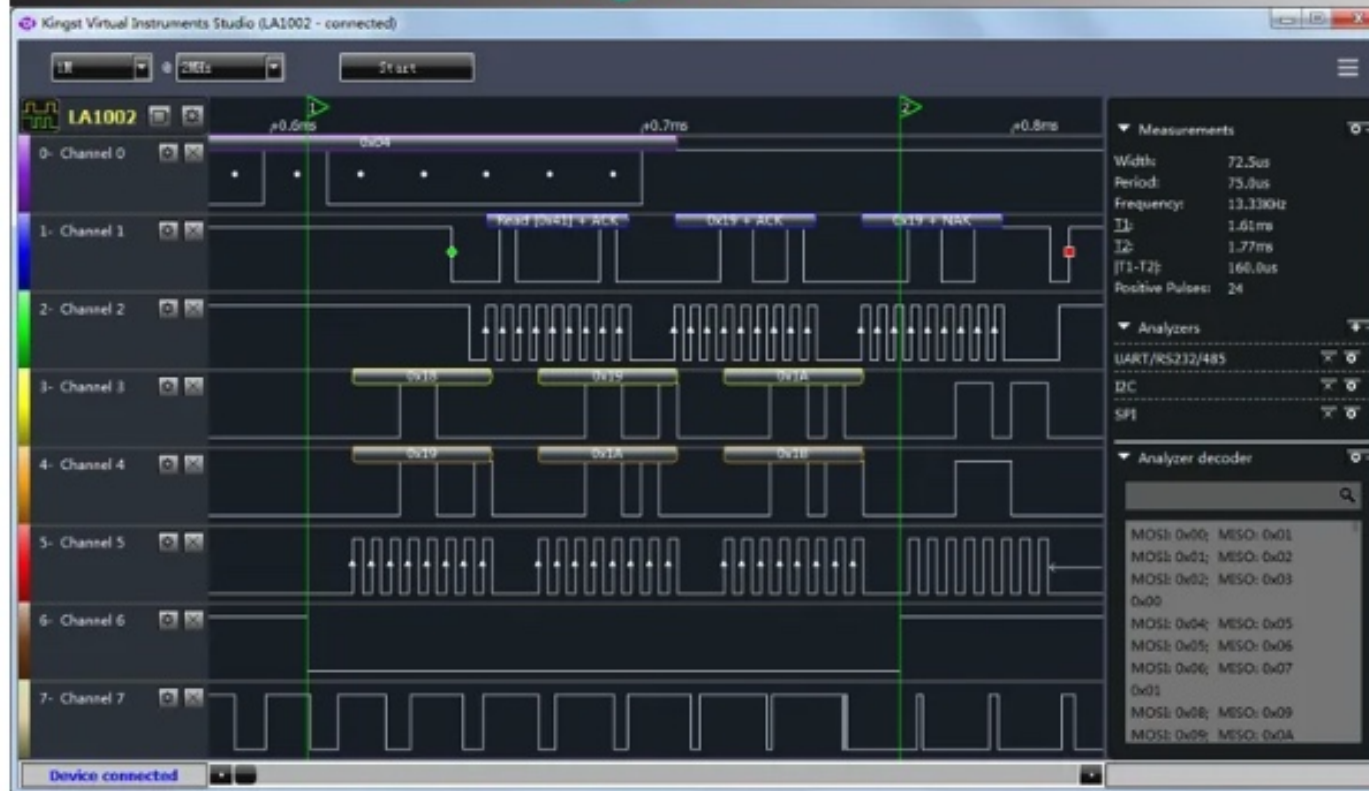
Shipping list :

1. LA5016 Logic Analyzer x1
2. 9P Test Lead Set x2
3. 2P Test Lead Set x1
4. Test Hook Clip x20
5. USB2.0 Cable x1
6. CD-ROM x1

LAX016 Series technical specification

Product type		LA1016	LA2016	LA5016
Input	Number of channels	16	16	16
	Max sampling rate	100MHz	200MHz	500MHz
	Measurement bandwidth	20MHz	40MHz	80MHz
	Min detectable pulse width	20ns	12.5ns	6.25ns
	Hardware storage size	1Gbits	1Gbits	1Gbits
	Hardware sampling depth	50M/channel	50M/channel	50M/channel
	Max compression depth	10G/channel	10G/channel	10G/channel
	Input voltage range	-50V~+50V	-50V~+50V	-50V~+50V
	Input impedance	220K Ω , 12pF	220K Ω , 12pF	220K Ω , 12pF
	Threshold voltage	Adjustable: -4~+4V Min step: 0.01V	Adjustable: -4~+4V Min step: 0.01V	Adjustable: -4~+4V Min step: 0.01V
PWM Output	Number of channels	2	2	2
	Output frequency range	0.1~20MHz	0.1~20MHz	0.1~20MHz
	Min step of period	10ns	10ns	10ns
	Min step of pulse width	5ns	5ns	5ns
	Output voltage	+3.3V	+3.3V	+3.3V
	Output impedance	50 Ω	50 Ω	50 Ω
Power supply	Power supply port	USB2.0/3.0	USB2.0/3.0	USB2.0/3.0
	Standby current	130mA	150mA	200mA
	Max active current	260mA	300mA	400mA
PC software	Supported protocols	UART/RS-232/485, I2C, SPI, CAN, DMX512, HDMI CEC, I2S/PCM, JTAG, LIN, Manchester, Midi, Modbus, 1-Wire, UNI/O, SDIO, SMBus, SWD, USB1.1, PS/2, NEC Infrared, Parallel, etc.		
	Supported OS	Windows XP、Vista、Windows 7/8/10(32bit/64bit)		

Powerful software — Kingst VIS



1. English Software,
Support Windows XP/7/8/10 (32/64bits)、MAC OS 、 Linux。
2. User friendly interface, Powerful analysis function
3. Supported decoder: UART/RS232/485, I2C, SPI, CAN, SDIO, DMX512, I2S/PCM, JTAG, LIN, Manchester, Modbus, 1-Wire, SMBus, UNI/O, USB1.1, NEC InfraRed, PS/2, Parallel, etc...
4. Data display format: Decimal/hex/bin/ASCII
5. Support data export, for save or other software
6. Free online automatic upgrade

csv format export example :

	A	B	C	D
1	Time [s]	Packet ID	MOSI	MISO
2	0.0001s	1	0x00	0x01
3	0.00018s	1	0x01	0x02
4	0.00026s	1	0x02	0x03
5	0.000516s	2	0x04	0x05
6	0.000596s	2	0x05	0x06
7	0.000676s	2	0x06	0x07

SPI protocol

	A	B	C	D
1	Time [s]	Value	Parity Error	Framing Error
2	0.0010416s	0x00	Error	
3	0.0031232s	0x01		
4	0.0052048s	0x02		
5	0.0072864s	0x03	Error	
6	0.009368s	0x04		
7	0.0114496s	0x05	Error	

UART protocol

	A	B	C	D	E	F
1	Time [s]	Packet ID	Address	Data	Read/Write	ACK/NAK
2	0.0000816s	0	0x41	0x00	Read	ACK
3	0.0001272s	0	0x41	0x00	Read	NAK
4	0.0002872s	2	0x41	0x01	Read	ACK
5	0.0003328s	2	0x41	0x01	Read	NAK
6	0.0004928s	4	0x41	0x02	Read	ACK
7	0.0005384s	4	0x41	0x02	Read	NAK

I2C protocol