



Parameter	Ratings	Units
Blocking Voltage	30	V_p
Load Current	2	A_{rms} / A_{DC}
On-Resistance (max)	0.15	Ω

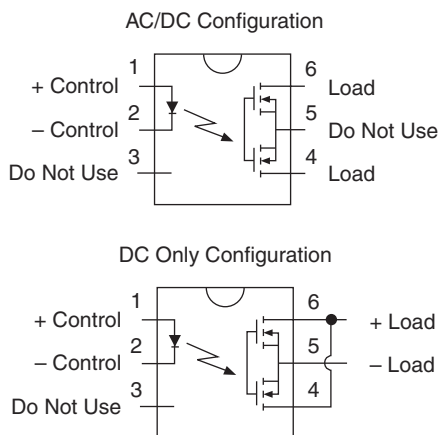
Features

- Very Low Maximum On-Resistance: 0.15 Ω
- High Load Current: 2A
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 6-Pin DIP Package
- Machine Insertable, Wave Solderable
- Surface Mount, Tape & Reel Version Available

Applications

- Sensor Circuitry
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment: Patient/Equipment Isolation
- Aerospace
- Industrial Controls

Pin Configuration



Description

The LCA717 is a 30V single-pole, normally open (1-Form-A) Solid State Relay. It uses optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation, and features an ultra-low on-resistance for high-current operation.

Its optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient GaAlAs infrared LED.

Offering the superior reliability associated with semiconductor devices, the LCA717 can be used to replace mechanical relays. Because it has no moving parts, it can offer faster, bounce-free switching in a more compact surface mount or thru-hole package.

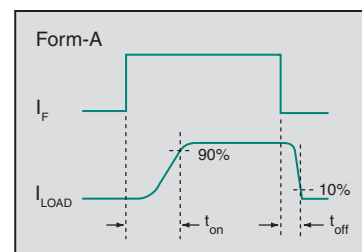
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: TUV Certificate B 09 07 49410 006

Ordering Information

Part #	Description
LCA717	6-Pin DIP (50/Tube)
LCA717S	6-Pin Surface Mount (50/Tube)
LCA717STR	6-Pin Surface Mount (1000/Reel)

Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	30	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	100	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

Electrical Characteristics @ 25°C

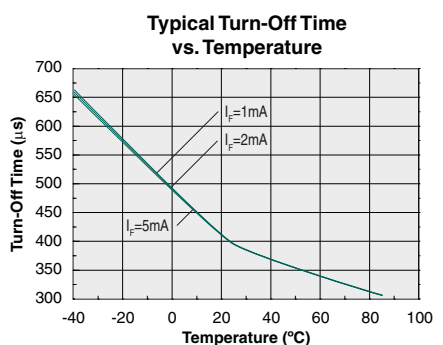
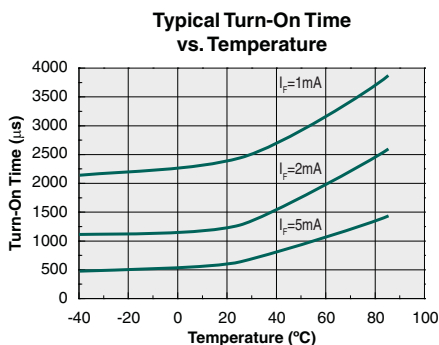
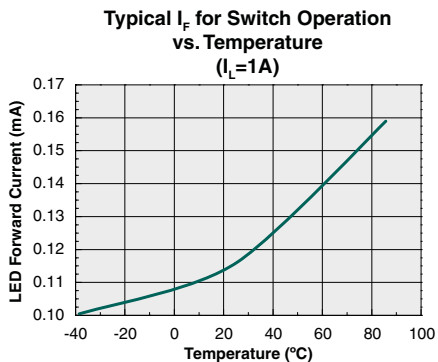
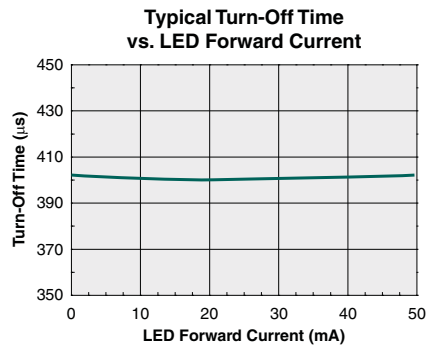
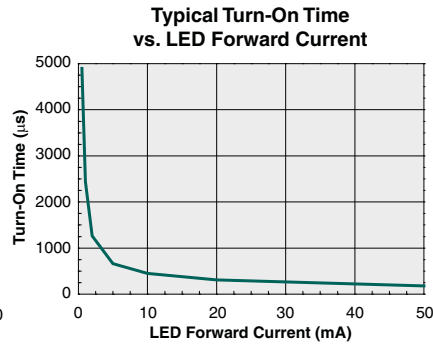
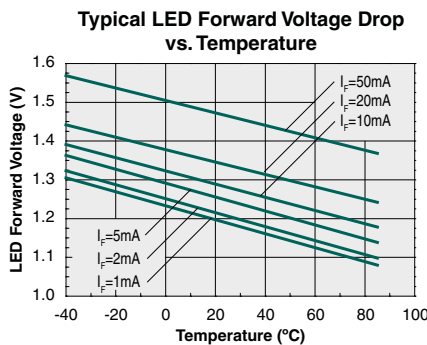
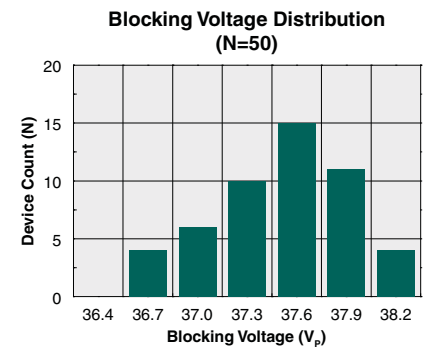
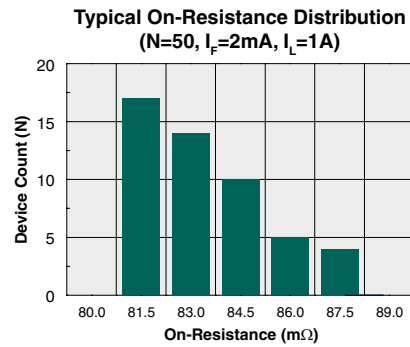
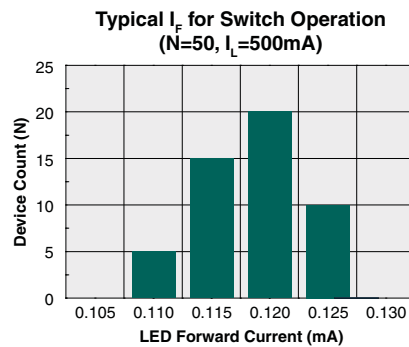
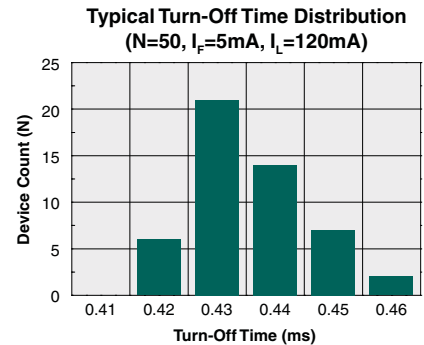
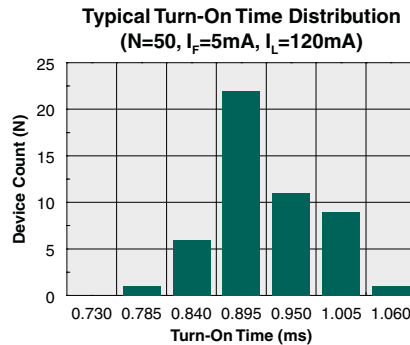
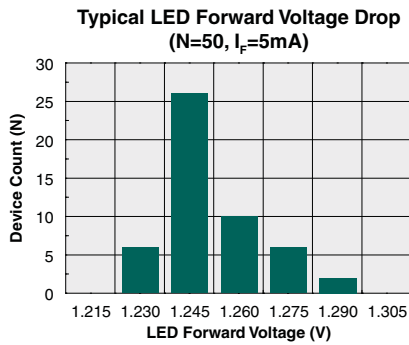
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current, Continuous						
AC/DC Configuration	-	I _L	-	-	2	A _{rms} / A _{DC}
DC Configuration					4	A _{DC}
Peak Load Current	t=10ms	I _{LPK}	-	-	±5	A _P
On-Resistance ¹						
AC/DC Configuration	I _L =1A	R _{ON}	-	0.083	0.15	Ω
DC Configuration				0.023	0.04	
Off-State Leakage Current	V _L =30V _P	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	t _{on}	-	0.92	3	ms
Turn-Off		t _{off}		0.44	3	
Output Capacitance	V _L =15V, f=1MHz	C _{OUT}	-	100	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =1A	I _F	-	0.115	2	mA
Input Control Current to Deactivate	-	I _F	0.1	-	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

¹ Measurement taken within 1 second of on-time.

ESD Rating

ESD Rating (Human Body Model)
1000 Volts

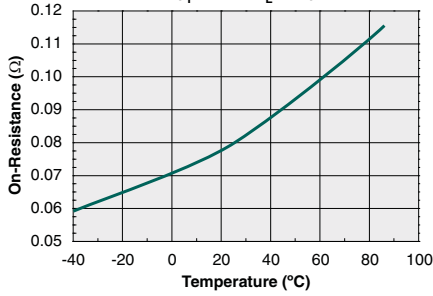
PERFORMANCE DATA @25°C (Unless Otherwise Noted)*



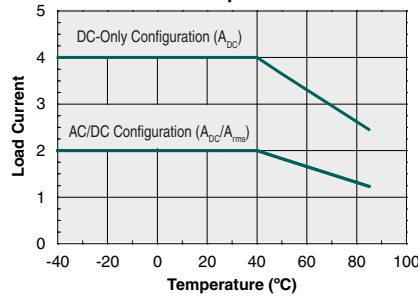
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA @25°C (Unless Otherwise Noted)*

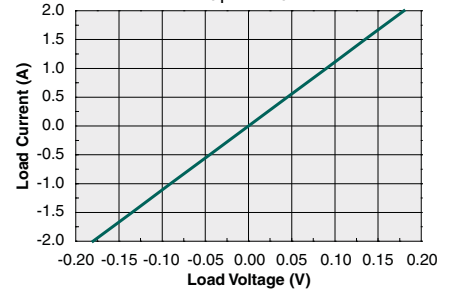
Typical On-Resistance vs. Temperature
($I_F=2\text{mA}$, $I_L=1\text{A}$)



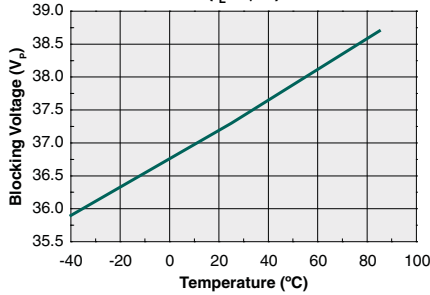
Maximum Load Current vs. Temperature



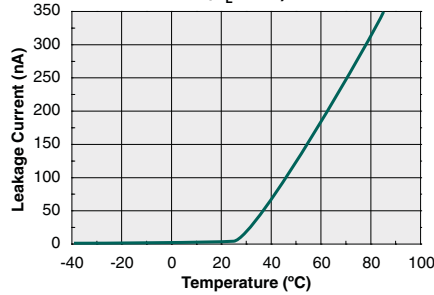
Typical Load Current vs. Load Voltage
($I_F=2\text{mA}$)



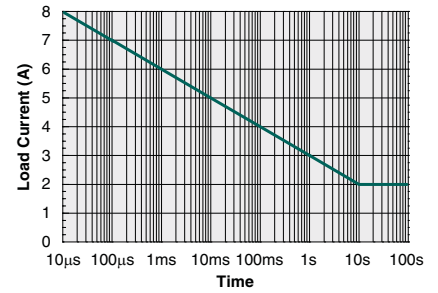
Typical Blocking Voltage vs. Temperature
($I_L=1\mu\text{A}$)



Typical Leakage vs. Temperature Measured Across Pins 4&6
($V_L=30\text{V}$)



Energy Rating Curve



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information
Moisture Sensitivity


All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
LCA717 / LCA717S	MSL 1

ESD Sensitivity


This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
LCA717 / LCA717S	250°C for 30 seconds

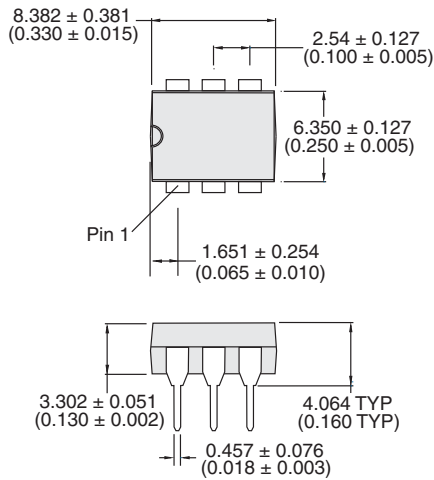
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

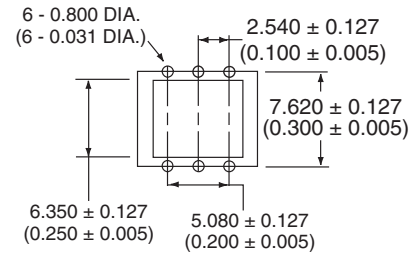


MECHANICAL DIMENSIONS

LCA717

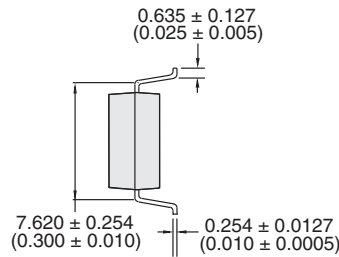
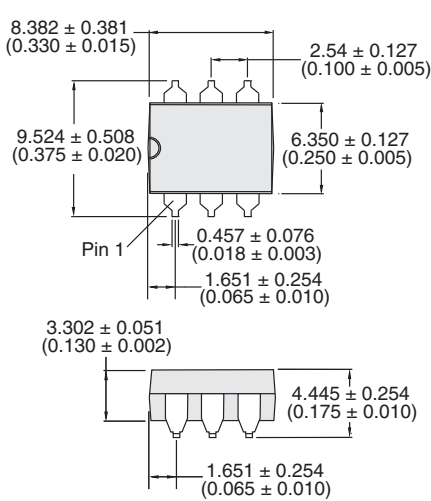


PCB Hole Pattern

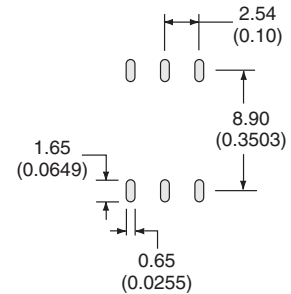


Dimensions
mm
(inches)

LCA717S

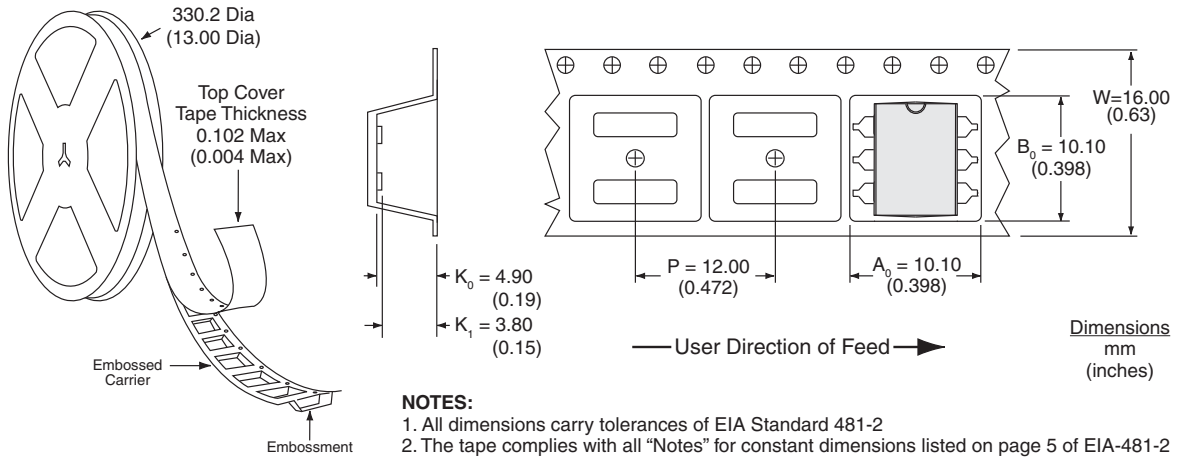


PCB Land Pattern



Dimensions
mm
(inches)

LCA717STR Tape & Reel



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