Integrated Silicon Pressure Sensor On-Chip Signal Conditioned, Temperature Compensated and Calibrated

The MPX4250D series piezoresistive transducer is a state-of-the-art monolithic silicon pressure sensor designed for a wide range of applications, particularly those employing a microcontroller or microprocessor with A/D inputs. This transducer combines advanced micromachining techniques, thin-film metallization, and bipolar processing to provide an accurate, high-level analog output signal that is proportional to the applied pressure. The small form factor and high reliability of on-chip integration make the Freescale sensor a logical and economical choice for the automotive system engineer.

Features

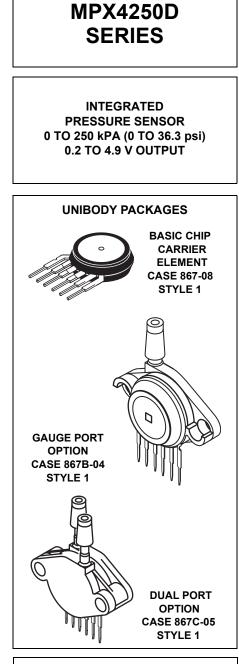
- Differential and Gauge Applications Available
- 1.4% Maximum Error Over 0° to 85°C
- Patented Silicon Shear Stress Strain Gauge
- Temperature Compensated Over –40° to +125°C
- Offers Reduction in Weight and Volume Compared to Existing Hybrid Modules
- Durable Epoxy Unibody Element

Typical Applications

Ideally Suited for Microprocessor or Microcontroller-Based Systems

ORDERING INFORMATION ⁽¹⁾							
Device Type Case No. MPX Series Order No. Device							
UNIBODY PACKAGE (MPX4250D SERIES)							
Basic Element 867 MPX4250D MPX4250D							
Gauge Ported Element	867B	MPX4250GP	MPX4250GP				
Dual Ported Element	867C	MPX4250DP	MPX4250DP				

 The MPX4250D series silicon pressure sensors are available in the basic element package or with pressure port fittings that provide mounting ease and barbed hose connections.



PIN NUMBERS ⁽¹⁾					
1 V _{out} 4 N/C					
2	GND	5	N/C		
3	V _S	6	N/C		

^{1.} Pins 4, 5, and 6 are internal device connections. Do not connect to external circuitry or ground. Pin 1 is noted by the notch in the lead.



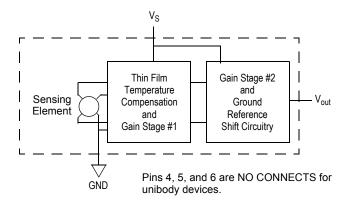


Figure 1. Fully Integrated Pressure Sensor Schematic

Table 1. Maximum Ratings⁽¹⁾

Rating	Symbol	Value	Unit
Maximum Pressure (P1 > P2)	P _{MAX}	1000	kPa
Storage Temperature	T _{STG}	-40 to +125	٦°
Operating Temperature	T _A	-40 to +125	°C

1. Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Characteristic		Symbol	Min	Тур	Мах	Unit
Pressure Range ⁽¹⁾			0	_	250	kPa
Supply Voltage ⁽²⁾		V _S	4.85	5.1	5.35	Vdc
Supply Current		ا _o	_	7.0	10	mAdc
Minimum Pressure Offset @ $V_S = 5.1 \text{ Volts}^{(3)}$	(0 to 85°C)	V _{off}	0.139	0.204	0.269	Vdc
Full Scale Output @ V_S = 5.1 Volts ⁽⁴⁾	(0 to 85°C)	V _{FSO}	4.844	4.909	4.974	Vdc
Full Scale Span @ V _S = 5.1 Volts ⁽⁵⁾	(0 to 85°C)	V _{FSS}	_	4.705		Vdc
Accuracy ⁽⁶⁾	(0 to 85°C)	—	_	—	±1.4	%V _{FSS}
Sensitivity		ΔV/ΔΡ	—	18.8		mV/kPa
Response Time ⁽⁷⁾		t _R	—	1.0		ms
Output Source Current at Full Scale Output		I _{o+}		0.1		mAdc
Warm-Up Time ⁽⁸⁾			_	20		ms
Offset Stability ⁽⁹⁾		— —	_	±0.5		%V _{FSS}

Table 2. Operating Characteristics ($V_S = 5.1$ Vdc, $T_A = 25^{\circ}C$ unless otherwise noted, P1 > P2. Decoupling circuit shown in Figure 3 required to meet electrical specifications.)

1. 1.0 kPa (kiloPascal) equals 0.145 psi.

2. Device is ratiometric within this specified excitation range.

3. Offset (Voff) is defined as the output voltage at the minimum rated pressure.

4. Full Scale Output (V_{FSO}) is defined as the output voltage at the maximum or full rated pressure.

 Full Scale Span (V_{FSS}) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.

6. Accuracy (error budget) consists of the following:

- Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.
- Temperature Hysteresis: Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.
 Pressure Hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from the
- TcSpan: minimum or maximum rated pressure, at 25°C.
 TcSpan: Output deviation over the temperature range of 0 to 85°C, relative to 25°C.
- TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85°C, relative to 25°C.
- Variation from Nominal: The variation from nominal values, for Offset or Full Scale Span, as a percent of V_{FSS}, at 25°C.
- 7. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 8. Warm-up Time is defined as the time required for the product to meet the specified output voltage after the Pressure has been stabilized.
- 9. Offset Stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

Table 3. Mechanical Characteristics

Characteristics	Тур	Unit
Weight, Basic Element (Case 867)	4.0	grams

ON-CHIP TEMPERATURE COMPENSATION AND CALIBRATION

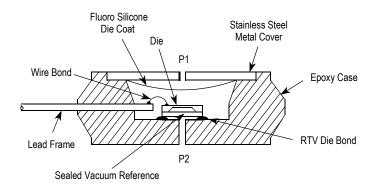
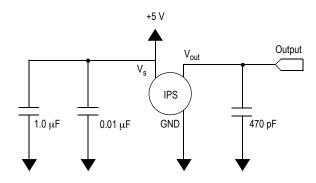
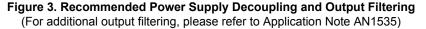


Figure 2. Cross Sectional Diagram (not to scale)





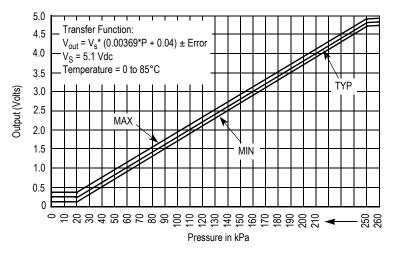


Figure 4. Output versus Absolute Pressure

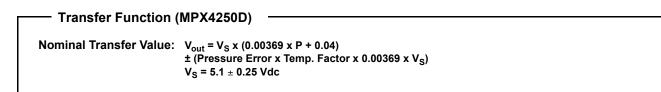
Figure 2 illustrates the differential/gauge pressure sensing chip in the basic chip carrier (Case 867). A fluorosilicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the sensor diaphragm.

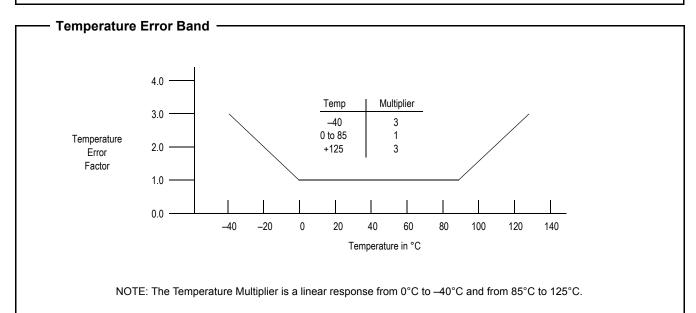
The MPX4250D series pressure sensor operating characteristics and internal reliability and qualification tests are based on use of dry air as the pressure media. Media, other than dry air, may have adverse effects on sensor performance and long-term reliability. Contact the factory for information regarding media compatibility in your application.

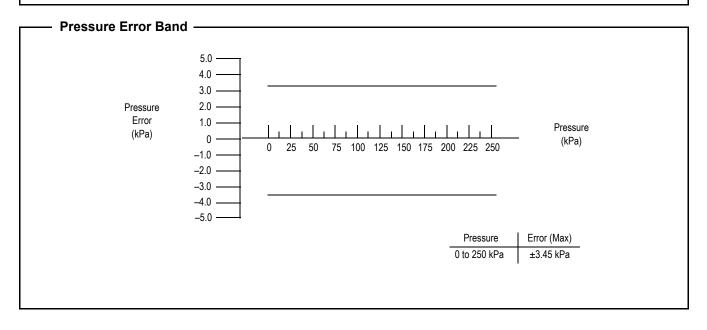
Figure 3 shows the recommended decoupling circuit for interfacing the output of the integrated sensor to the A/D input of a microprocessor or microcontroller.

Figure 4 shows the sensor output signal relative to pressure input. Typical, minimum, and maximum output curves are shown for operation over a temperature range of 0° to 85°C using the decoupling circuit shown in Figure 3. The output will saturate outside of the specified pressure range.

MPX4250D

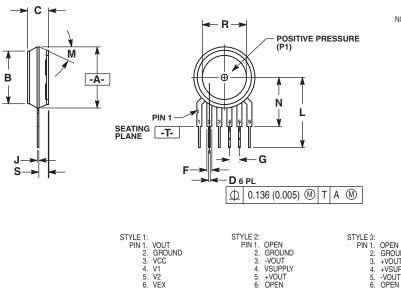






MPX4250D

PACKAGE DIMENSIONS



NOTES:

GROUND +VOUT +VSUPPLY -VOUT OPEN

 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2

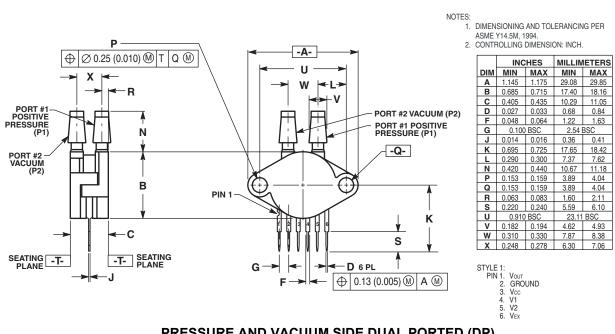
CONTROLLING DIMENSION: INCH. DIMENSION -A- IS INCLUSIVE OF THE MOLD STOP RING. MOLD STOP RING NOT TO EXCEED 3 16.00 (0.630).

	INCHES		MILLIM	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.595	0.630	15.11	16.00	
В	0.514	0.534	13.06	13.56	
С	0.200	0.220	5.08	5.59	
D	0.027	27 0.033 0.68		0.84	
F	0.048 0.064		1.22		
G	0.100	BSC	2.54 BSC		
J	0.014	0.016	0.36	0.40	
L	0.695 0.725		17.65	18.42	
Μ	30° 1	MON	30° NOM		
Ν	R 0.430 0.450 10.9		12.07	12.57	
R			10.92	11.43	
S			2.29	2.66	

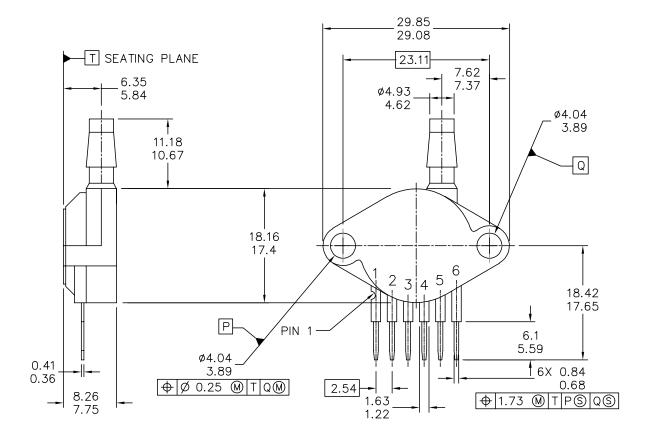
STYLE 1: PIN 1. VOUT 2. GROUND 3. VCC 4. V1 5. V2 6. VEX

51	C ELE	(D)
	GROUND -VOUT VSUPPLY +VOUT OPEN	2. 3. 4. 5. 6.
	UPEN	PIN I.

BAS CASE 867-08 **ISSUE N**



PRESSURE AND VACUUM SIDE DUAL PORTED (DP) CASE 867C-05 **ISSUE F**



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TITLE:		DOCUMENT NO]: 98ASB42796B	RE∨⊧G
SENSOR, 6 LEAD UNIBODY CELL, AP & GP 01ASB09087B		CASE NUMBER: 867B-04 28 JUL 20		28 JUL 2005
		STANDARD: NO	IN-JEDEC	

PAGE 1 OF 2

PRESSURE SIDE PORTED (GP) CASE 867B-04 ISSUE G

PACKAGE DIMENSIONS

NOTES:

- 1. DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
- 3. 867B-01 THRU -3 OBSOLETE, NEW STANDARD 867B-04.

STYLE 1:

PIN 1: V OUT 2: GROUND 3: VCC 4: V1 5: V2 6: V EX

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TITLE: SENSOR, 6 LEAD UNIBODY CELL, AP & GP 01ASB09087B		DOCUMENT NO): 98ASB42796B	REV: G
		CASE NUMBER	R: 867B-04	28 JUL 2005
		STANDARD: NO	DN-JEDEC	

PAGE 2 OF 2

PRESSURE SIDE PORTED (GP) CASE 867B-04 ISSUE G

MPX4250D

Sensors Freescale Semiconductor

NOTES

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