# WisNode-SPI EVB Quick Start Guide

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# 1. Product introduction

### 1.1 Overview

WisNode-SPI module is based on the RAK439 module design of an Arduino-compatible development board, which inherited the RAK439 the SPI interface, supporting STM32F4, STM32F1 chip can be the fastest to 8Mb / s throughput speed, used for large data communications Suitable, of course, RAK439 low-power mode will allow the module does not need to transmit data, reduce the overall power consumption, saving electricity.

## **1.2** Evaluation Kit introduction

After purchasing the Arduino WisNode-SPI EVB evaluation kit on Taobao, we will send it with the following RAK official designation. As shown in Figure 1-1. The items in the box are shown in Figure 1-2: WisNode-SPI EVB 2.4GHZ Antenna.



Fig 1-1





## 1.3 Hardware introduction

The following figure is the RAK WisNode series SPI development board, compatible Arduino development board, supporting STM32F411 NUCLEO-F411RE development board with the use of. Interface resources are as follows:



Function	Name	Note
Module	U2	RAK439 Module
External interface	Micro USB	Power Supply DC 5V input
Key	Reset	Reset button for the backplane STM32(spare)
Pin out	Р2	Function pin (spare)
LED Light	POWER	Power Indicator



## 2. Quick to use

#### 2.1 Ready to work

Here WisNode-SPI module and STM32 NUCLEO-F411RE supporting the use of the program can be used in this link to download: <u>https://github.com/RAKWireless/WisNode-SPI</u>

The required WisNode-SPI development board can be purchased at the following Taobao link:<u>https://www.aliexpress.com/store/product/WisNode-SPI-development-board-compatible-Arduino-board-6M-s</u> peed-low-power-fast-test-Open-Source-Harware/2805180\_32789744834.html?spm=2114.12010608.0.0.ddcjRT

The required NUCLEO-F411RE backplane can be purchased at this Taobao link : <u>https://www.aliexpress.com/wholesale?catId=0&initiative\_id=SB\_20170220185531&SearchText=NUCLEO-F41</u> <u>1RE</u>





# 2.2 Example demonstration

After the preparation is complete, we have three samples at this time: development board, floor, program. This time we need to download the program to the floor. How to download? Please refer to the following steps:

1.Because our program is written using keil MDK tools, so users need to have keil MDK software, the software version of the best in 5.1 or more. Keil MDK software download link: http://www2.keil.com/mdk5/install

2.Download installation completed keil MDK, also need to install ST-Link driver to drive the floor work. Drive in ST official website download.

http://www.st.com/content/st\_com/en/products/embedded-software/development-tool-software/stsw-link009.html

#### GET SOFTWARE

Part Number	Software Version	Marketing Status	Supplier	Order from ST
STSW-LINK009	1.02	Active	ST	Get Software

Driver Installation Please refer to STM32 Nucleo's documentation for STM32 Nucleo boards User manual.

3.After the installation of the driver, through the USB interface to connect the floor and the computer, and then insert the development board on the floor, as shown below:





RAKASO STM32Edvy NOS-SDK 1.0.4-ard

#### ETDX160624135

4.Unzip the program and use the keil MDK software to open the examples\_nos \ TestSpeed \ project \ MDK\_ARM \ RAK439.uvprojx project file under the RAK439\_STM32F4xx\_NOS-SDK\_1\_0\_4-arduino folder. And then click the red box shown below to configure the module Debug settings.

File Edit View Project Flash Debug Periph	erals Tools SVCS Window Help
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E 🍕 Project: RAK439	1 #include "rw app.h"
😑 🔂 RAK439	2
🕀 🗁 common/include	3 int platform init(void)
nw_error.h	4 🖬 1
nv rocket h	5 rw DriverParams t params;
e common/mem	6 int ret =0;
i lib_mem.c	7 char libVersion[20]="";
lib_mem.h	8 char module_mac[6] ="";
🖃 🦢 examples_nos/tcp_udp	9
⊕ iii ap_sta.c	10 host_platformInit();
⊕ <u>u</u> tcp_udp.c	<pre>11 DPRINTF("Host platform initsuccess\r\n");</pre>
TertSpeed c	12
platform stm32f4xx/bsp	13 //rak module driver init
🐵 🗋 bsp.c	14 wifi_init_params(&params);
- D bsp.h	15 ret =rw sysDriverInit(&params);
- stm32f4xx.h	16 If (ret != RW_OK)
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rak_wrti_nos_ARM_CM4EJib	25
	26 return RW OK;
	28 (10) 51x)
	29
	30 int main (void)
	31 🗄 {
1	32 int ret = 0;
Project Books () Functions 0, Templates	
Find In Files	
( ·	
Build Output	
	ST-Link Debugger Li26 C16 CAP. NUM. SCRL, OVR. R/V

5. Click on the following settings, set the Debug configuration: (If there is no corresponding option, it may be ST-Link driver is not installed successfully, please re-install the ST-Link driver)

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			Xtal (MHz):	2.0	-Code C	Compiler:	Use default	compiler versio	n 🔻
Operating	; system:	None		•			1		
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	IPOM2-		- [	- C	E	IRAM2		· [	



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Use Simulator with restrictions Settings	I Use: ST-Link Debugger ▼ Settings		
Z Load Application at Startup	4  O Application at Startup  ✓ Run to main() Initialization File:		
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Restore Debug Session Settings	Restore Debug Session Settings		
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6.After setting, click on the compiler, after the success of the compiler can click to download the program. Keil MDK software compiler, download the program button as shown below:

确定

取消

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- bsp.h	15	6			
stm32f4xx.h	16 RW_APP_CTX app_demo_ctx;	( 🖣			
stm32f4xx_conf.h	17				
⊞ istm32f4xo_it.c	18 void rw_appdemo_context_init(void)				
stm32f4xx_it.h	19 甲 {				
	20 int i =0;				
startup_stm32H11xe.s	21				
platform_stm32r4x0/libraries	<pre>22 memset((void*)&amp;app_demo_ctx, 0, sizeof(app_demo_ctx));</pre>				
P ny lib platform c	<pre>23 app_demo_ctx.ltcps_sockfd = INVAILD_SOCK_FD;</pre>				
ak wife nos ARM CM4E lib	<pre>24 app_demo_ctx.ludps_sockfd = INVAILD_SOCK_FD;</pre>				
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Build Output Find In Files Browser					
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7.After the program is downloaded successfully, reset the STM32F411 backplane and WisNode-SPI development board, and then search for a SSID: RAK439\_AP WIFI signal, password: 1234567890. Use the computer connected to the WIFI, open the TCP / UDP test tool, select the establishment of TCP Client, IP address: 192.168.7.1, port: 25000.

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8.After the success of the establishment of the module will always send data to the tool, then open the test speed tool, you can view the maximum transfer rate RAK439. (The speed is variable, the maximum 8Mb / s)





# 3. Appendix





# 4. Modify Record

Version	Author	Data	Modify content
V1.0	caoxiaocheng	2016/12/28	Create Document
V1.1	xc.c	2017/02/21	Updata Document
V1.2	xc.c	2017/02/28	Modify some description and picture