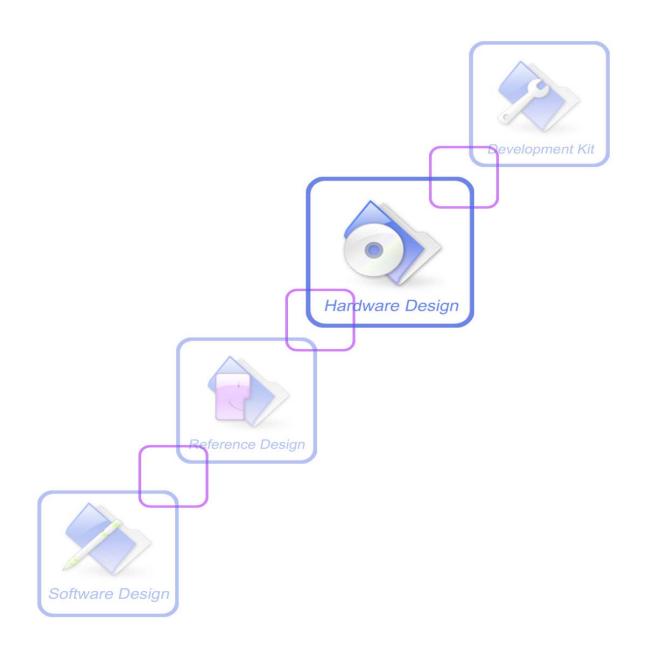


SIM28_Hardware Design_V1.02





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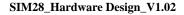
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Version History

| Date | Version | Description of change | Author |
|------------|---------|-----------------------------------|---------------------------|
| 2011-06-25 | V1.00 | Origin | Jing.zhou jianmei.zhou |
| | | | Jianiner.znou |
| 2011-09-19 | V1.01 | Delete internal LNA | Honggang ma |
| 2011-09-22 | V1.02 | Add LNA, and correct RF parameter | Honggang ma |
| | | | Jing zhou |



1 Introduction

This document describes the hardware interface of the SIMCom module SIM28 which can be used as a stand alone or A-GPS (Assisted Global Positioning System) receiver. As a wide range of applications can be integrated in SIM28, all functional components of SIM28 are described in great detail.

2 SIM28 Overview

SIM28 is a stand-alone or A-GPS receiver. With built-in LNA, SIM28 can relax antenna requirement and don't need for external LNA. SIM28 can track as low as -165dBm signal even without network assistance. The SIM28 has excellent low power consumption characteristic (acquisition 24mA, tracking 19mA). SIM28 supports various location and navigation applications, including autonomous GPS, SBAS ranging (WAAS, EGNOS, GAGAN, MSAS), DGPS (RTCM), and A-GPS.

Key Features

With a tiny configuration of 16 x 12.2 x 2.4 mm package, SIM28 can meet almost all the space requirements in your applications.

The module provides complete signal processing from antenna input to host port in either NMEA messages. The module requires 2.9V~3.6V power supply. The host port is configurable to UART. Host data and I/O signal levels are 2.85V CMOS compatible.

2.1 SIM28 Functional Diagram

The following figure shows a functional diagram of the SIM28 and illustrates the mainly functional parts:

- The GPS chip
- SAW filter
- The antenna interface
- The communication interface
- The control signals



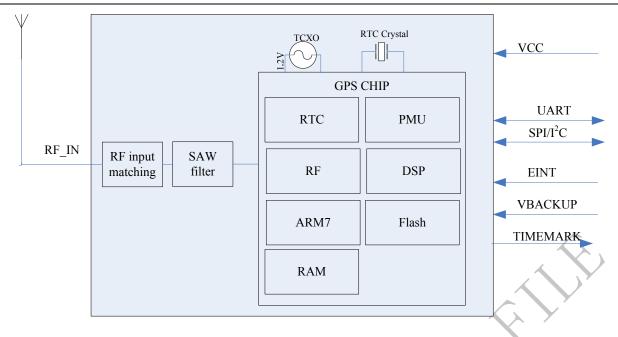


Figure 1: SIM28 functional diagram

2.2 GPS Performance

Table 1: GPS performance

| Parameter | Description | Performance | | | |
|--|-------------------------|-------------|------|-------|---------|
| Parameter | Description | Min | Type | Max | Unit |
| Horizontal Position Accuracy ⁽¹⁾ | Autonomous | | <2.5 | | m |
| Velocity | Without Aid | | 0.1 | | m/s |
| Accuracy ⁽²⁾ | DGPS | | 0.05 | | m/s |
| Acceleration | Without Aid | | 0.1 | | m/s^2 |
| Accuracy | DGPS | | 0.05 | | m/s^2 |
| Timing Accuracy | | | 10 | | nS |
| Dynamic | Maximum Altitude | | | 18000 | m |
| Performance | Maximum Velocity | | | 515 | m/s |
| | Maximum Acceleration | | | 4 | G |
| Time To First Fix ⁽³⁾ | Hot start | | <1 | | S |
| | Warm start | | 30 | | S |
| | Cold start | | 32 | | S |
| A-GPS TTFF(EPO | Hot start | | 0.7 | | S |
| in flash mode) | Warm start | | 1.5 | | S |
| | Cold start | | 12.5 | | S |
| Sensitivity | Autonomous | | -147 | | dBm |
| | acquisition(cold start) | | | | |
| | Re-acquisition | | -160 | | dBm |
| | Tracking | | -165 | | dBm |
| Receiver | Channels | | 66 | | |



| | Update rate | | 10 | Hz |
|----------------------------|---------------------|-----|----|----|
| | Tracking L1, CA | | | |
| | Code | | | |
| | Protocol support | | | |
| | NMEA,PMTK | | | |
| Power | Continuous tracking | 19 | | mA |
| consumption ⁽⁴⁾ | Sleep current | 200 | | uA |

(1) 50% 24hr static, -130dBm

(2) 50% at 30m/s

(3) GPS signal level: -130dBm(4) Single Power supply 3V

2.3 General features

Table 2: General features

| | | <u> </u> | | |
|-----------------------|----------------|---|--|--|
| Parameters | | Value | | |
| Supply voltage VCC | | +2.9V~3.6V | | |
| Supply voltage ripp | ole VCC | 54 mV(RMS) max @ $f = 0 \sim 3MHz$ | | |
| | | 15 mV(RMS) max @ f > 3 MHz | | |
| Power consumption | n(acquisition) | 24mA type. @ VCC=3 V | | |
| Power consumption | n(sleep) | 200uA type. @ VCC=3 V | | |
| Storage temperature | e | -40°C~+85°C | | |
| Operating temperat | ure | -30°C~+85°C (note 1) | | |
| I/O signal levels VIL | | -0.3V~0.8V | | |
| | VIH | 2.0V~3.6V | | |
| | VOL | -0.3V~0.4V | | |
| | VOH | 2.4V~3.1V | | |
| I/O output sink/sou | rce capability | +/- 3mA max | | |
| I/O input leakage | | +/- 10 uA max | | |
| Host port | | UART, | | |
| Other port | | I ² C/SPI | | |
| Serial port protocol | (UART) | NMEA; 8 bits, no parity, 1 stop bit; 115200 baud (configurable) | | |
| TM output (1PPS) | | 1 pulse per second, synchronized at rising edge, pulse length 300ms | | |

Note 1: Operation in the temperature range $-40^{\circ}\text{C} \sim -30^{\circ}\text{C}$ is allowed but Time-to-First-Fix performance and tracking sensitivity may be degraded.



3 Package Information

3.1 Pin out Diagram

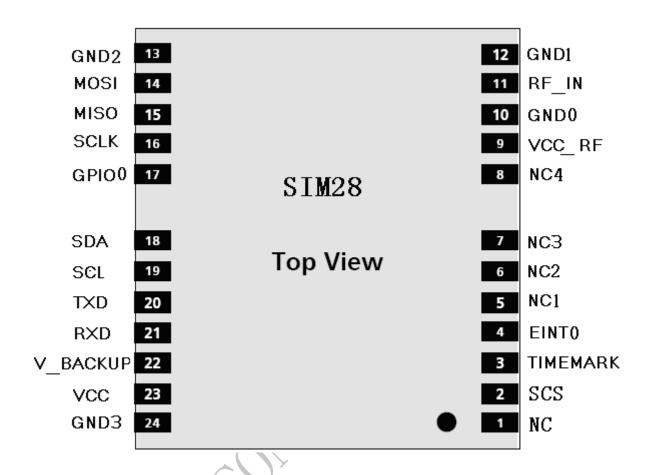


Figure 2: SIM28 pin out diagram (Top view)

3.2 Pin Description

Table 3: Pin description

| Pin name | Pin number | I/O | Description | Comment | | |
|--------------|------------|-----|---|--|--|--|
| Power supply | | | | | | |
| VCC | 23 | Ι | Main power input, which will be used to power the baseband and RF section internally. | Provide clean and stable power source to this pin. Add a 4.7uF capacitor to this pin for decoupling. | | |
| VCC_RF | 9 | О | 2.8V output power supply for active antenna | If unused, keep open. | | |
| V_BACKUP | 22 | I | The backup battery input power supply for RTC | If unused, keep open. | | |



| GND | 10,12,13,24 | | Ground | GND |
|--------------------|-------------|-----|---|-----------------------|
| Host port interfac | e | | | |
| MISO | 15 | I | SPI MISO | |
| MOSI | 14 | O | SPI MOSI | If unused, keep open. |
| SCLK | 16 | O | SPI clock | ii unusea, keep open. |
| SCS | 2 | O | SPI slave select | |
| SDA | 18 | I/O | I ² C data | If unused, keep open. |
| SCL | 19 | I/O | I ² C C Clock | ii unused, keep open. |
| TXD | 20 | O | Serial output | |
| RXD | 21 | I | Serial input | |
| GPIOs | | | | |
| EINT0 | 4 | I | This interrupt source could act as wake up event during power saving mode. Provide an interrupt on either high or low logic level or edge-sensitive interrupt | If unused, keep open. |
| TIMEMARK | 3 | О | Time Mark outputs timing pulse related to receiver time | If unused, keep open. |
| GPIO0 | 17 | I/O | GPIO can provide the developers signal or message outputs. GPIO lines supports a simple control interface. | If unused, keep open. |
| RF interface | | | | |
| RF_IN | 11 | I | Radio antenna connection | |
| Other interface | | | | |
| NC | 1,5,6,7,8 | | RESERVED | |

3.3 Package Dimensions

Following figure shows the Mechanical dimensions of SIM28 (top view, side view and bottom view).



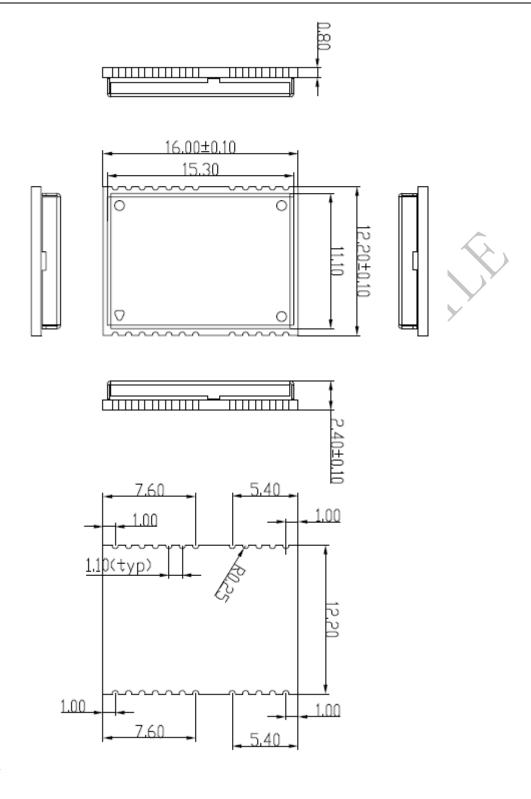


Figure 3: SIM28 mechanical dimensions (Unit: mm)



3.4 SIM28 Recommended PCB Decal

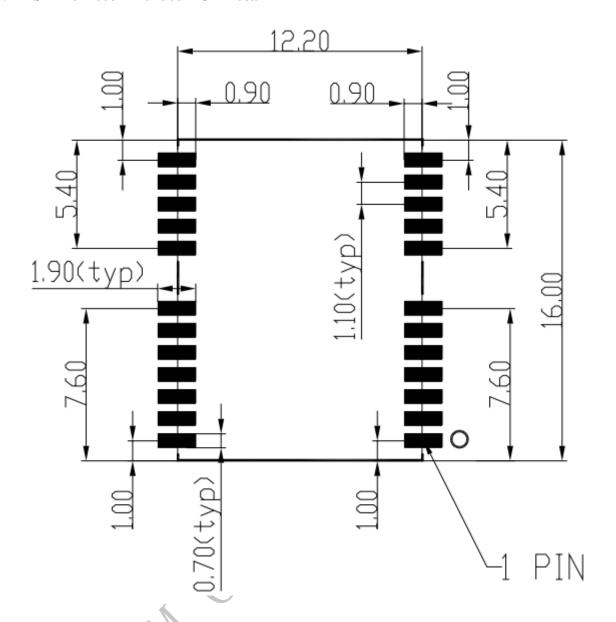


Figure 4: Recommended PCB decal (top view) (Unit: mm)



4 Application Interface

4.1 Power Management

4.1.1 Power Input

The power supply range of SIM28 is from 2.9V to 3.6V. The power supply should be able to provide sufficient current up to 50mA.

4.1.2 Starting SIM28

For initial power up, the RTC must start oscillating to sequence the Finite State Machine. RTC startup time may vary.

• When power is first applied, SIM28 goes into operation mode.

4.1.3 Verification of SIM28 Start

System activity indication depends upon the chosen serial interface:

• When it is activated, SIM28 will output messages at the selected UART speed and message types.

4.1.4 Power Saving Modes

SIM28 supports operating modes for reduced average power consumption like standby mode, backup mode, periodic mode, and AlwayLocateTM mode.

- Standby mode: In this mode the receiver stays at full on power state. When this mode that can be wake up by the host sends the command through the communication interface or external interrupt.
- Backup mode: In this mode the SIM28 must be supplied by the backup and it can help to count down the time for backup mode. Software on host side to send the command through the communication interface to into the backup mode.
- Periodic mode: In this mode the SIM28 enters tracking and backup modes according to the interval configured by users in the commands.
- AlwayLocateTM mode: AlwayLocateTM is an intelligent controller of SIM28 periodic mode. Depending on
 the environment and motion conditions, SIM28 can adaptive adjust the on/off time to achieve balance of
 positioning accuracy and power consumption.

SIM28 provides very low leakage battery back up memory, which contains all the necessary GPS information for quick start up and a small amount of user configuration variables. It needs a 3V power supply for V_BACKUP pin, and the stable operation region ranges from very light load to about 3mA.



4.1.5 Operating Mode

Table 4: Power supply and clock state according to operation mode

| Mode | VCC | Main clock | RTC clock |
|---------|-----|------------|-----------|
| Full on | on | on | on |
| Sleep | on | off | on |

4.1.5.1 Full on Mode

The module will enter full on mode after first power up with factory configuration settings. Power consumption will vary depending on the amount of satellite acquisitions and number of satellites in track. This mode is also referenced as Full on, Full Power or Navigation mode.

Navigation is available and any configuration settings are valid as long as the VCC power supply is active. When the power supply is off, settings are reset to factory configuration and receiver performs a cold start on next power up.

4.1.5.2 Sleep Mode

Sleep mode means a low quiescent (200uA type.) power state, non-volatile RTC, and backup RAM block is powered on. Other internal blocks like digital baseband and RF are internally powered off. The power supply input VCC shall be kept active all the time, even during sleep mode. Waking up from and entering into sleep mode is controlled by host interrupt EINTO.

4.1.6 VCC RF

VCC_RF is a 2.8V output for external active antenna, if the external active antenna works at 2.8V voltage supply domain, the user can use the VCC_RF directly. If the antenna's power is not 2.8V, it should be open. For passive antennas, VCC_RF should be open.

4.2 UART Interface

SIM28 includes one configurable UART interface for serial communication. This UART is as NEMA output and PMTK command input. The receiver (RXD) and transmitter (TXD) side of every port contains a 16-byte FIFO and has 256 bytes URAM. The bit rates are selectable and ranging from 4.8 to 921.6kbps. UART can provide the developers signal or message outputs.

Table 5: Host port multiplexed function pins

| Pin name | Pin number | UART function |
|----------|------------|---------------|
| TXD | 20 | data transmit |
| RXD | 21 | data receive |



4.3 SPI Interface

The SPI interface allows for connection of external serial flash to save configuration and A-GPS data. The SCS chip select signal is available to select external slaves. External SPI serial flash up to 128Mbits is supported.

Table 6: SPI function pins

| Pin name | Pin number | SPI function |
|----------|------------|---------------|
| MISO | 15 | Master input |
| MOSI | 14 | Master output |
| SCLK | 16 | Clock output |
| SCS | 2 | Chip select |

4.4 I²C interface

The SCL and SDA are the I^2C bus pins, which can be connected to a external I^2C interface EEPROM up to 1 Mbits for reading and writing data into EEPROM. This can be used to store configurations permanently.

NOTE: The EEPROM and flash can't be supported at the same time.

4.5 Timemark Output

The Timemark pin outputs pulse-per-second (1PPS) pulse signal for precise timing purposes. The Timemark signal can be provided through designated output pin for many external applications. This pulse is not only limited to be active every second but also allowed to set the required duration, frequency, and active high/low by programming user-defined settings.

4.6 A-GPS

The SIM28 supports the EPO (Extended Prediction Orbit) data service. The EPO data service is supporting 7/14/30-day orbit predictions to customers. It needs occasional download from EPO server. Supply of aiding information like ephemeris, almanac, rough last position and time and satellite status and an optional time synchronization signal will reduce time to first fix significantly and improve the acquisition sensitivity.

The user should update the EPO files from the EPO server daily through the internet. Then the EPO data should send to the SIM28 by the HOST side. SIM28 has the short cold TTFF and warm TTFF, when the A-GPS is used.

4.7 GPS Antenna

The SIM28 is designed for use with passive and active antennas.

Table 7: Antenna Specifications



| Parameter | Specification | Passive and active antenna | |
|--------------------------------|-----------------|----------------------------|--|
| | Frequency range | 1575±3MHz | |
| Active Antenna Recommendations | Polarization | RHCP | |
| Active Antenna Recommendations | Gain | >20dB (max 50 dB) | |
| | Noise Figure | <1.5 dB | |

4.7.1 Antenna Interface

The SIM28 receives L1 band signals from GPS and GALILEO satellites at a nominal frequency of 1575.42 MHz. The RF signal is connected to the RF_IN pin. And the trace from RF_IN to antenna should be 50Ω controlled.

To suit the physical design of individual applications the RF interface pad can lead to three alternatives:

- Recommended approach: solderable RF coaxial cable assembly antenna connector, such as HRS'
 U.FL-R-SMT(10) connector or I-PEX's 20279-001E-01 RF connector.
- SMA connector.

4.7.2 GPS Antenna Choice Consideration

To obtain excellent GPS reception performance, a good antenna will always be required. The antenna is the most critical item for successful GPS reception in a weak signal environment. Proper choice and placement of the antenna will ensure that satellites at all elevations can be seen, and therefore, accurate fix measurements are obtained.

4.7.2.1 Passive Antenna

Passive antenna contain only the radiating element, e.g. the ceramic patch, the helix structure, and chip antennas. Sometimes they also contain a passive matching network to match the electrical connection to 50 Ohms impedance.

The most common antenna type for GPS applications is the patch antenna. Patch antennas are flat, generally have a ceramic and metal body and are mounted on a metal base plate.

Figure 5 shows a minimal setup for a PVT GPS receiver with SIM28 module.



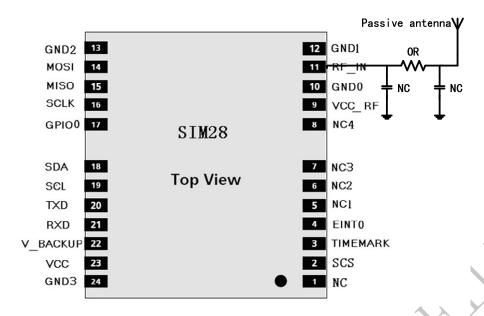


Figure 5: SIM28 passive antenna design

For best performance with passive antenna designs user can use an external LNA to increase the sensitivity up 3~4 dB. Please see Figure 6 and Figure 7.

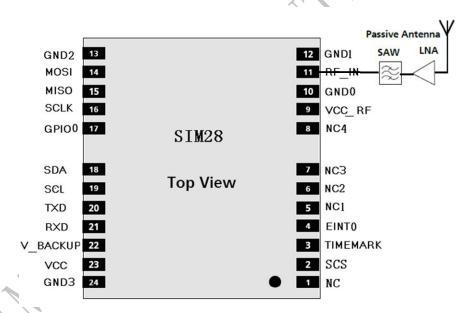


Figure 6: SIM28 passive antenna design (with external LNA and SAW)



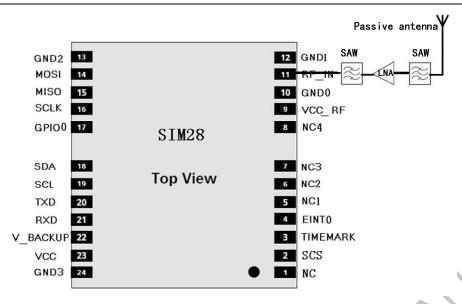


Figure 7: SIM28 passive antenna design for best performance and increased immunity

4.7.2.2 Active Antennas

Active antennas have an integrated Low-Noise Amplifier (LNA). Active antennas need a power supply that will contribute to GPS system power consumption. Usually, the supply voltage is fed to the antenna through the coaxial RF cable shown as Figure 8. The output voltage of PIN 9 is 2.8V. If the supply voltage of active antenna is 2.8V, PIN 9 VCC_RF can be used as V_ANT.

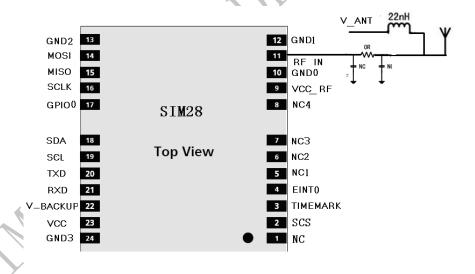


Figure 8: SIM28 Active antenna design

If the customer's design is for automotive applications, then an active antenna can be used and located on top of the car in order to guarantee the best signal quality.

GPS antenna choice should base on the designing product and other conditions. For detailed Antenna designing consideration, please refer to related antenna vendor's design recommendation. The antenna vendor will offer further technical support and tune their antenna characteristic to achieve successful GPS reception performance depending on the customer's design.



5 Electrical, Reliability and Radio Characteristics

5.1 Absolute Maximum Ratings

The absolute maximum ratings stated in Table 7 are stress ratings under non-operating conditions. Stresses beyond any of these limits will cause permanent damage to SIM28.

Table 8: Absolute maximum ratings

| Parameter | Min | Max | Unit |
|-----------------------|-------|------|---------------|
| VCC | - | 4.3 | V |
| VCC_ANT | -5.5V | +5.5 | V |
| RF_IN | - | 3.08 | V |
| V_BACKUP | - | 4.3 | V |
| I/O pin voltage | - | 3.6 | V |
| Storage temperature | -45 | +125 | ${\mathbb C}$ |
| Operating Temperature | -40 | +85 | $^{\circ}$ C |

5.2 Recommended Operating Conditions

Table 9: SIM28 operating conditions

| Parameter | Symbol | Min | Тур | Max | Unit |
|-------------------------------|----------|-----|-----|-----|------------|
| Operating temperature range | | -40 | +25 | +85 | $^{\circ}$ |
| Main supply voltage | VCC | 2.9 | 3 | 3.6 | V |
| Active antenna supply voltage | VCC_RF | 2.7 | 2.8 | 2.9 | V |
| output | Imax | | | 10 | mA |
| Backup battery voltage | V_BACKUP | 2 | | 3.6 | V |

Table 10: SIM28 standard IO features

| Parameter | Symbol | Min | Тур | Max | Unit |
|---|----------|------|-----|------|------|
| Low level output voltage Test conditions IOL = 2mA and 4.0mA | V_{ol} | -0.3 | | 0.40 | V |
| High level output voltage Test conditions IOL = 2mA and 4.0mA | V_{oh} | 2.4 | | 3.1 | V |
| Low level input voltage | V_{il} | -0.3 | | 0.8 | V |
| High level input voltage | V_{ih} | 2.0 | | 3.6 | V |
| Input Pull-up resistance | RPU | 40 | | 190 | ΚΩ |
| Input Pull-dowm resistance | RPD | 40 | | 190 | ΚΩ |
| Input capacitance | C_{in} | | 5 | | pF |



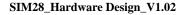
| Load capacitance | C_{load} | | 8 | pF |
|---------------------------|------------|-----|----|----|
| Tri-state leakage current | IOZ | -10 | 10 | uA |

5.3 Electro-Static Discharge

The GPS engine is not protected against Electrostatic Discharge (ESD) in general. Therefore, it is subject to ESD handing precautions that typically apply to ESD sensitive components. Proper ESD handing and packaging procedures must be applied throughout the processing, handing and operation of any application using a SIM28 module.

5.4 Certification

SIM28 meets the requirements of Directive 2002/95/EC of the European Parliament and of the Council on the Restriction of Hazardous Substance (RoHS).and has acquired CE certification.





6 Manufacturing

6.1 Top and Bottom View of SIM28





Figure 9: Top and bottom view of SIM28

6.2 Assembly and Soldering

The SIM28 module is intended for SMT assembly and soldering in a Pb-free reflow process on the top side of the PCB. Suggested solder paste stencil height is 150um minimum to ensure sufficient solder volume. If required paste mask pad openings can be increased to ensure proper soldering and solder wetting over pads. The following figure is the Ramp-Soak-Spike Reflow Profile of SIM28:

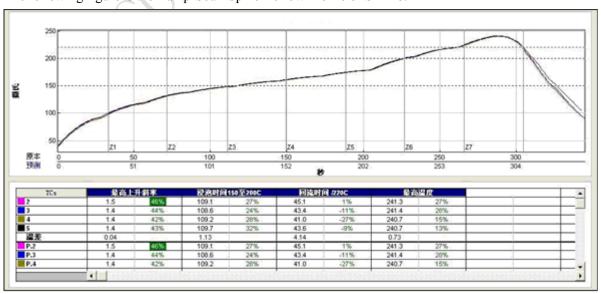


Figure 10: The Ramp-Soak-Spike reflow profile of SIM28



SIM28 is Moisture Sensitive Devices (MSD), appropriate MSD handling instruction and precautions are summarized in Chapter 6.3.

SIM28 modules are also Electrostatic Sensitive Devices (ESD), handling SIM28 modules without proper ESD protection may destroy or damage them permanently.

Avoid ultrasonic exposure due to internal crystal and SAW components.

6.3 Moisture sensitivity

SIM28 module is moisture sensitive at MSL level 3, dry packed according to IPC/JEDEC specification J-STD-020C. The calculated shelf life for dry packed SMD packages is a minimum of 12 months from the bag seal date, when stored in a non condensing atmospheric environment of <40°C/90% RH.

Table 10 lists floor life for different MSL levels in the IPC/JDEC specification:

Floor Life(out of bag)at factory ambient ≤ +30°C/60%RH or as stated Level 1 Unlimited at $\leq +30^{\circ}$ C/85% RH 1 year 4 weeks 2a 3 168 hours 4 72 hours 5 48 hours 5a 24 hours Mandatory bake before use. After bake, module must be reflowed within 6 the time limit specified on the label.

Table 11: Moisture Classification Level and Floor Life

Factory floor life is 1 week for MSL 3, SIM28 must be processed and soldered within the time. If this time is exceeded, or the humidity indicator card in the sealed package indicates that they have been exposed to moisture, the devices need to be pre-baked before the reflow solder process.

Both encapsulate and substrate materials absorb moisture. IPC/JEDEC specification J-STD-020 must be observed to prevent cracking and delamination associated with the "popcorn" effect during reflow soldering. The popcorn effect can be described as miniature explosions of evaporating moisture. Baking before processing is required in the following cases:

- Humidity indicator card: At least one circular indicator is no longer blue
- Floor life or environmental requirements after opening the seal have been exceeded, e.g. exposure to excessive seasonal humidity.

Refer to Section 4 of IPC/JEDEC J-STD-033 for recommended baking procedures.

Notes: Oxidation Risk: Baking SMD packages may cause oxidation and/or inter metallic growth of the terminations, which if excessive can result in solder ability problems during board assembly. The temperature and time for baking SMD packages are therefore limited by solder ability considerations. The cumulative bake time at a temperature greater than 90°C and up to 125°C shall not exceed 96 hours.



6.4 ESD handling precautions

SIM28 modules are Electrostatic Sensitive Devices (ESD). Observe precautions for handling! Failure to observe these precautions can result in severe damage to the GPS receiver!



GPS receivers are Electrostatic Sensitive Devices (ESD) and require special precautions when handling. Particular care must be exercised when handling patch antennas, due to the risk of electrostatic charges. In addition to standard ESD safety practices, the following measures should be taken into account whenever handling the receiver:

Unless there is a galvanic coupling between the local GND (i.e. the work Table) and the PCB GND, then the first point of contact when handling the PCB shall always be between the local GND and PCB GND.

Before mounting an antenna patch, connect ground of the device

When handling the RF pin, do not come into contact with any charged capacitors and be careful when contacting materials that can develop charges (e.g. patch antenna \sim 10pF, coax cable \sim 50-80pF/m, soldering iron, ...) To prevent electrostatic discharge through the RF input, do not touch the mounted patch antenna.

When soldering RF connectors and patch antennas to the receiver's RF pin, the user must make sure to use an ESD safe soldering iron (tip).

6.5 Shipment

SIM28 is designed and packaged to be processed in an automatic assembly line, and it is now packaged in SIM28 tray.

7 Reference Design

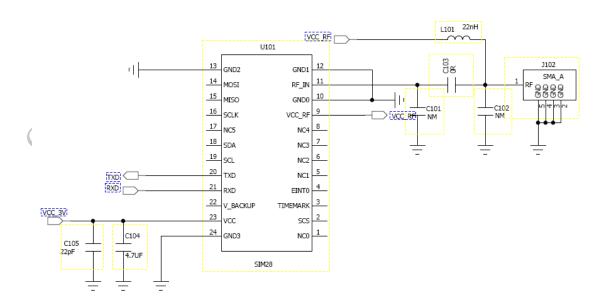


Figure 11: Example application schematic with UART



- 1) The pin VCC_RF provides a 2.8V output level for external active antenna. For active antenna, if antenna's power domain is 2.8V, the pin VCC_RF could connect to it directly. If the antenna's power is not 2.8v, keep the pin VCC_RF open For passive antenna, the pin VCC_RF should be kept open.
- 2) The maximum input ripple of VCC is as follows table 11





Appendix

A. Related Documents

Table 12: Related documents

| SN | Document name | Remark |
|-----|---------------|--------|
| [1] | SIM28_EVB-USG | |
| | | |
| | | |

B. Terms and Abbreviations

Table 13: Terms and abbreviations

| Abbreviation | Description |
|--------------|---|
| A-GPS | |
| CMOS | Complementary Metal Oxide Semiconductor |
| EEPROM | Electrically Erasable Programmable Read Only Memory |
| EPO | Extended Prediction Orbit |
| ESD | Electrostatic Sensitive Devices |
| FSM | Finite State Machine |
| GPS | Global Positioning System |
| I/O | Input/Output |
| IC | Integrated Circuit |
| Inorm | Normal Current |
| Imax | Maximum Load Current |
| kbps | Kilo bits per second |
| KA | Keep alive |
| MSL | moisture sensitive level |
| NEMA | National Marine Electronics Association |
| SGEE | server-generated extended ephemeris |



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