

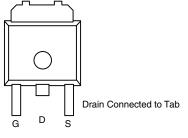
## SUD50P06-15

Vishay Siliconix

# P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω <b>)</b>	I <sub>D</sub> (A)		
- 60	0.015 at V <sub>GS</sub> = - 10 V	- 50 <sup>d</sup>		
	0.020 at V <sub>GS</sub> = - 4.5 V	- 50 <sup>d</sup>		





Top View
Ordering Information

SUD50P06-15-GE3 (Lead (Pb)-free and Halogen-free) SUD50P06-15-T4-GE3 (Lead (Pb)-free and Halogen-free)

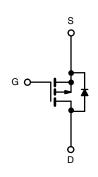
#### **FEATURES**

- TrenchFET<sup>®</sup> Power MOSFET
- Material categorization: For definitions of compliance please see <u>www.vishav.com/doc?99912</u>



#### **APPLICATIONS**

· Load Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V <sub>DS</sub>	- 60	V			
Gate-Source Voltage	V <sub>GS</sub>	± 20	v			
Continuous Drain Current (T <sub>.1</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I <sub>D</sub>	- 50 <sup>d</sup>	А		
Commods Drain Guirent (1j = 175 G)	T <sub>C</sub> = 125 °C	סי	- 27.5			
Pulsed Drain Current	I <sub>DM</sub>	- 80				
Avalanche Current		I <sub>AS</sub>	- 50			
Single Pulse Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	125	mJ		
Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	113 <sup>c</sup>	w		
Power Dissipation	T <sub>A</sub> = 25 °C		2.5 <sup>b, c</sup>	~~~~		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunstien te Ambient	t ≤ 10 s	R <sub>thJA</sub>	15	18	°C/W	
Junction-to-Ambient <sup>b</sup>	Steady State		40	50		
Junction-to-Case		R <sub>thJC</sub>	0.82	1.1		

Notes:

a. Duty cycle  $\leq$  1 %.

b. When mounted on 1" square PCB (FR-4 material).

c. See SOA curve for voltage derating.

d. Package limited.

# SUD50P06-15

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	•	·		•			
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, $I_D$ = - 250 $\mu$ A	- 60			v	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA - 1			- 3	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 60 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C			- 50	μΑ	
		$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$			- 100		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 50			А	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 17 A		0.012	0.015		
	P	$V_{GS}$ = - 10 V, I <sub>D</sub> = - 50 A, T <sub>J</sub> = 125 °C			0.025	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = - 10 V, I <sub>D</sub> = - 50 A, T <sub>J</sub> = 150 °C			0.028		
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 14 A			0.020		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 17 A		61		S	
Dynamic <sup>b</sup>		•					
Input Capacitance	C <sub>iss</sub>			4950			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$ , $V_{DS} = -25 V$ , f = 1 MHz		480		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			405			
Total Gate Charge <sup>c</sup>	Qg			110	165		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = -30$ V, $V_{GS} = -10$ V, $I_{D} = -50$ A		19		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			28			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			15	23		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = - 30 V, $R_L$ = 0.6 $\Omega$		70	105	20	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ - 50 A, $V_{GEN}$ = - 10 V, $R_G$ = 6 $\Omega$		175	260	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>	] [		175	260		
Source-Drain Diode Ratings and Cha	racteristics	Γ <sub>C</sub> = 25 °C <sup>b</sup>			·		
Continuous Current	ا <sub>S</sub>				- 50	٨	
Pulsed Current	I <sub>SM</sub>				- 80	A	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 50 A, V <sub>GS</sub> = 0 V		- 1	- 1.6	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 50 A, dl/dt = 100 A/μs		45	70	ns	

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

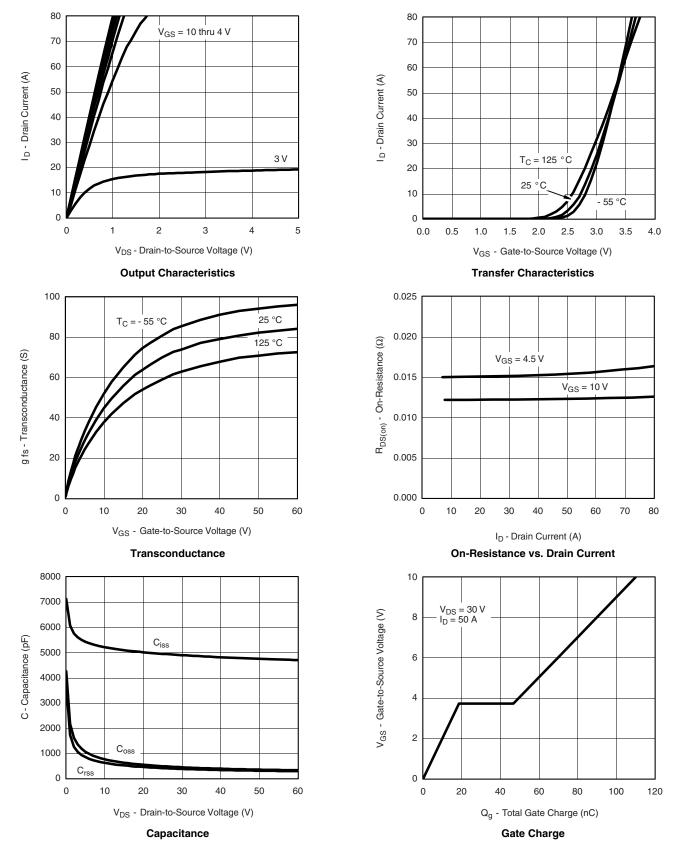
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



# SUD50P06-15

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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



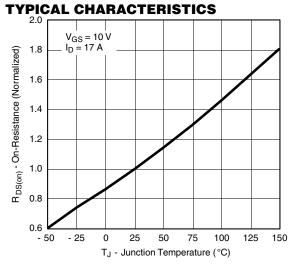
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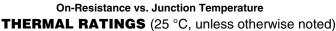
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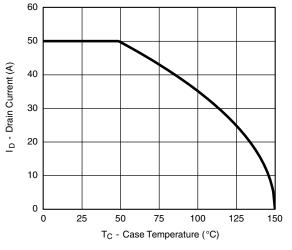
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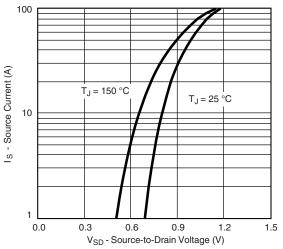
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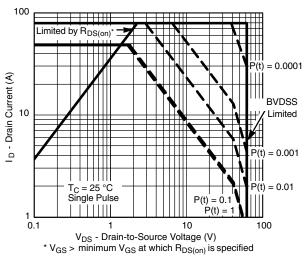


Drain Current vs. Case Temperature

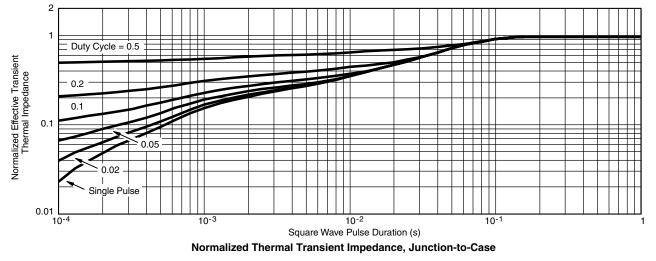


**ISHA** 

Source-Drain Diode Forward Voltage



Safe Operating Area



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?68940">www.vishay.com/ppg?68940</a>.

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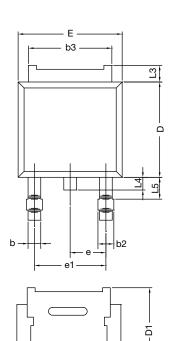
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**TO-252AA** Case Outline

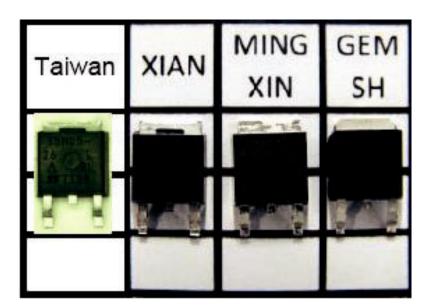
	MILLIN	<b>IETERS</b>	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
А	2.18	2.38	0.086	0.094		
A1	-	0.127	-	0.005		
b	0.64	0.88	0.025	0.035		
b2	0.76	1.14	0.030	0.045		
b3	4.95	5.46	0.195	0.215		
С	0.46	0.61	0.018	0.024		
C2	0.46	0.89	0.018	0.035		
D	5.97	6.22	0.235	0.245		
D1	4.10	-	0.161	-		
E	6.35	6.73	0.250	0.265		
E1	4.32	-	0.170	-		
Н	9.40	10.41	0.370	0.410		
е	2.28	BSC	0.090 BSC			
e1	4.56	BSC	0.180 BSC			
L	1.40	1.78	0.055	0.070		
L3	0.89	1.27	0.035	0.050		
L4	-	1.02	-	0.040		
L5	1.01	1.52	0.040	0.060		
ECN: T13-0359-Rev. O, 03-Jun-13 DWG: 5347						

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Notes

• Dimension L3 is for reference only.

• Xi'an, Mingxin, and GEM SH actual photo.



Revision: 03-Jun-13

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1



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#### **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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